

FIG. 1

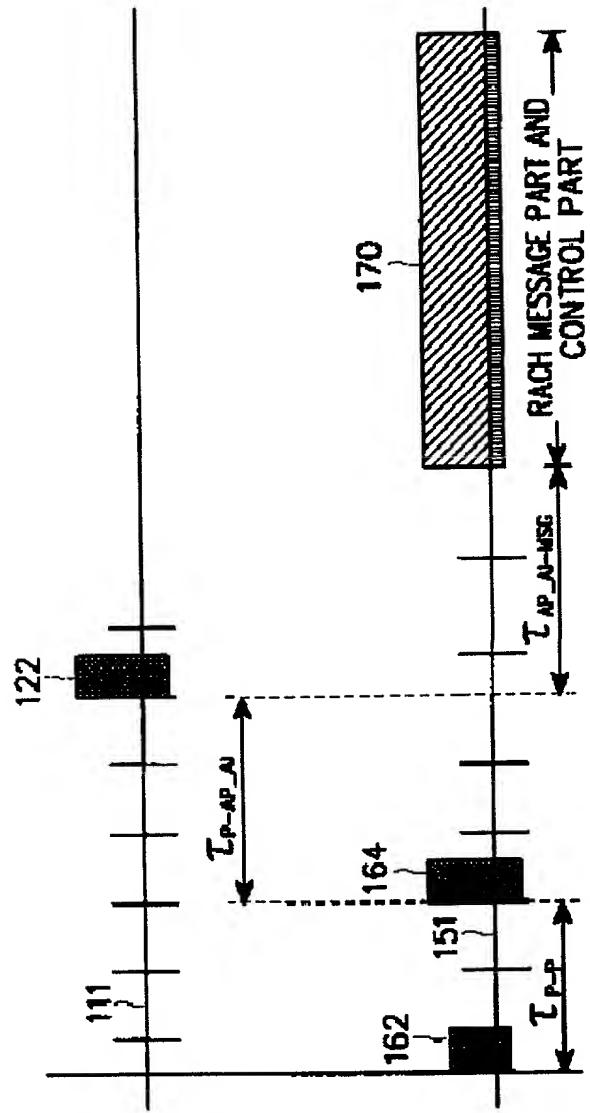


FIG. 2

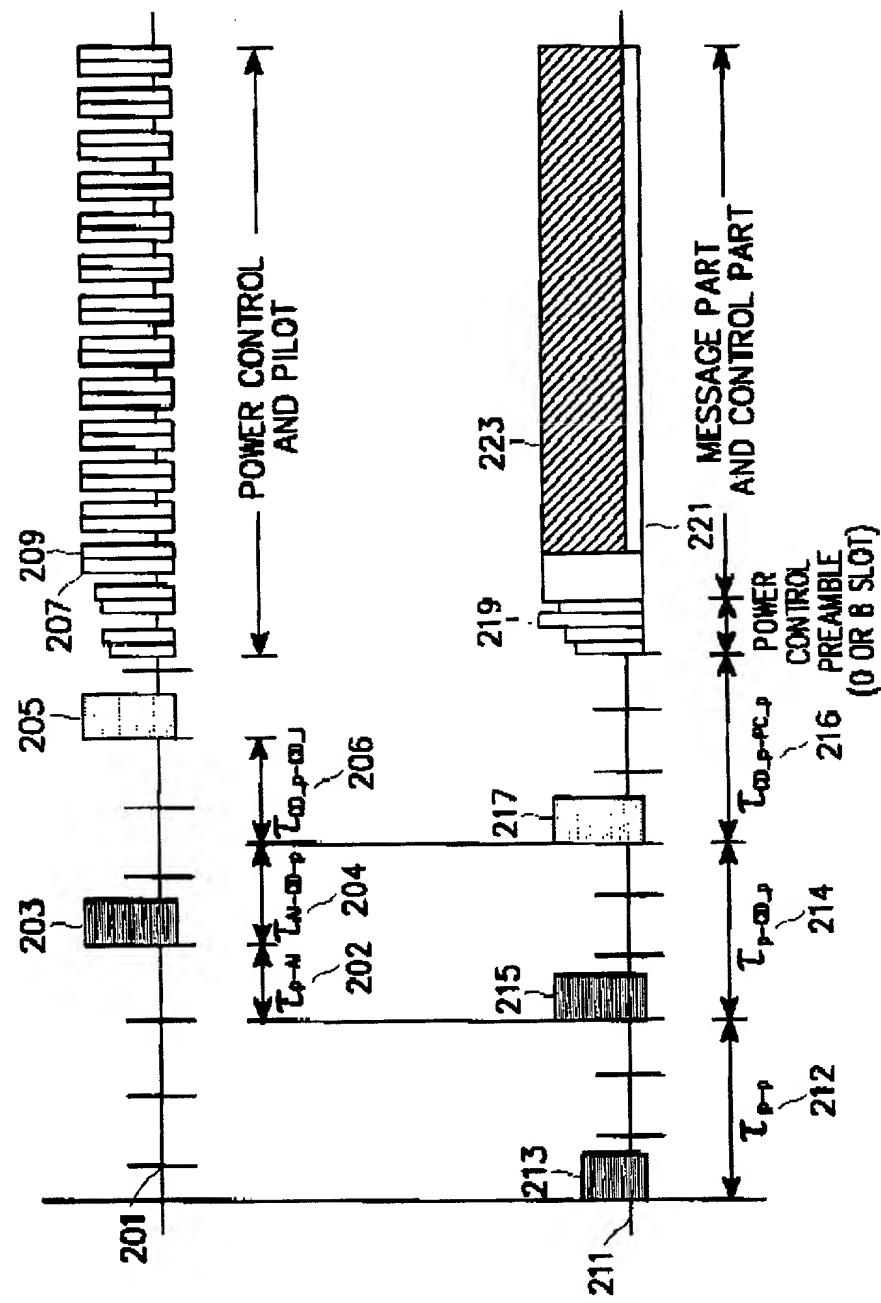


FIG. 3

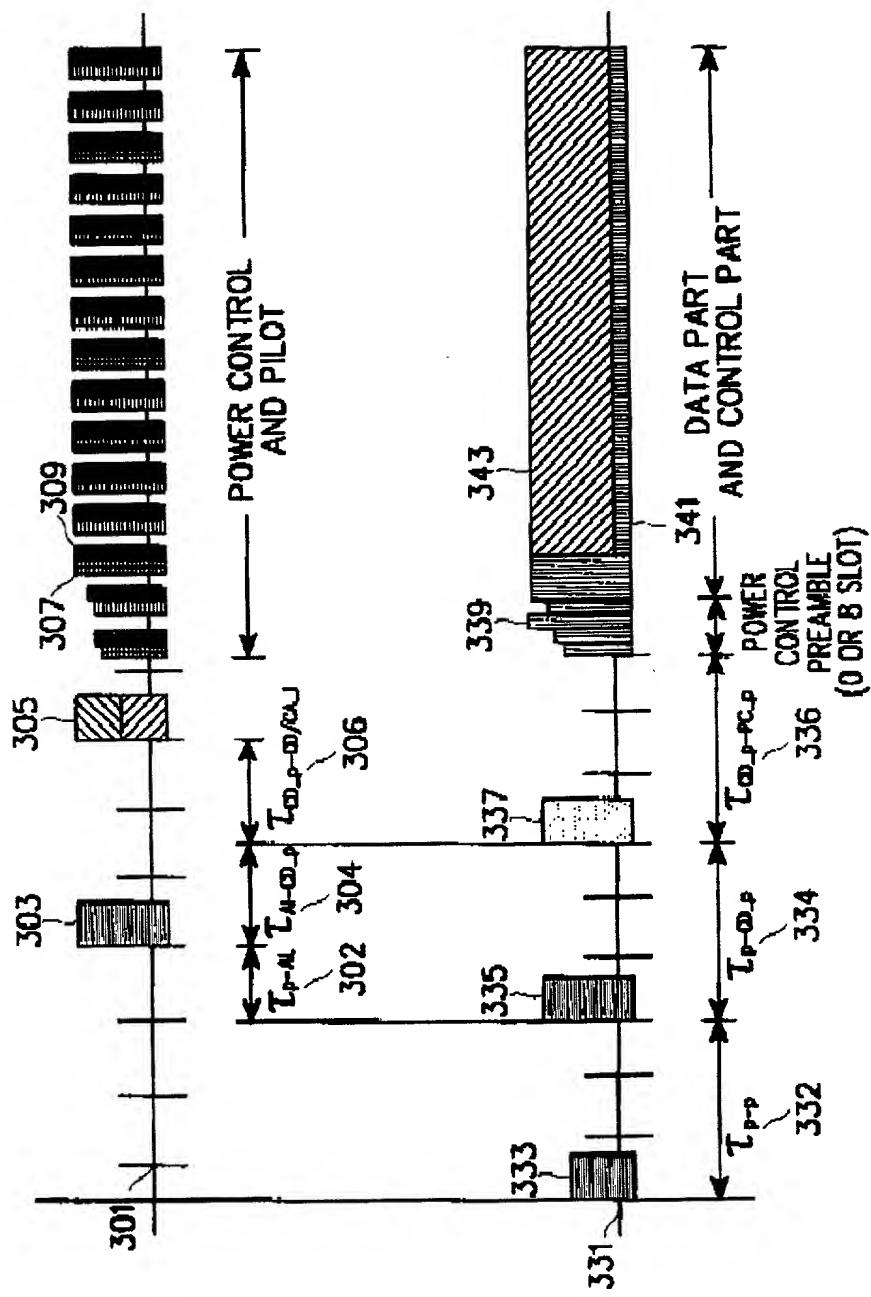
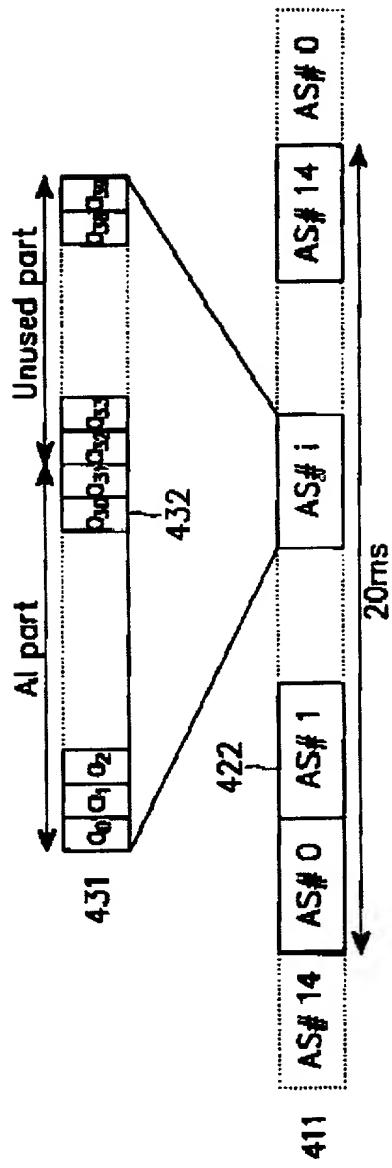


FIG. 4



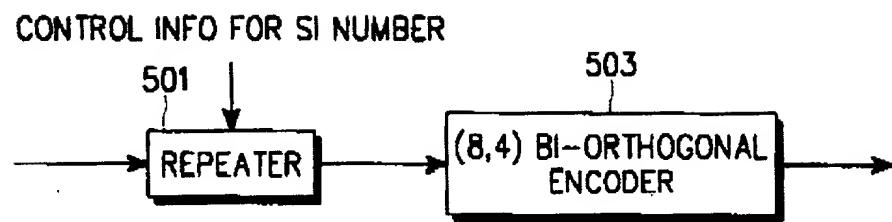


FIG. 5

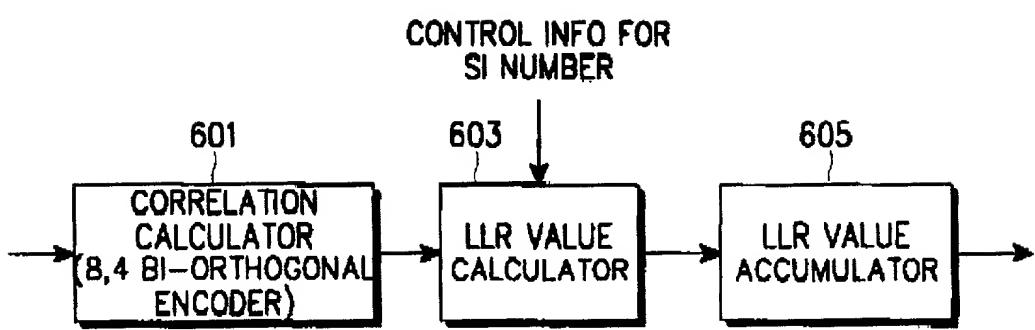
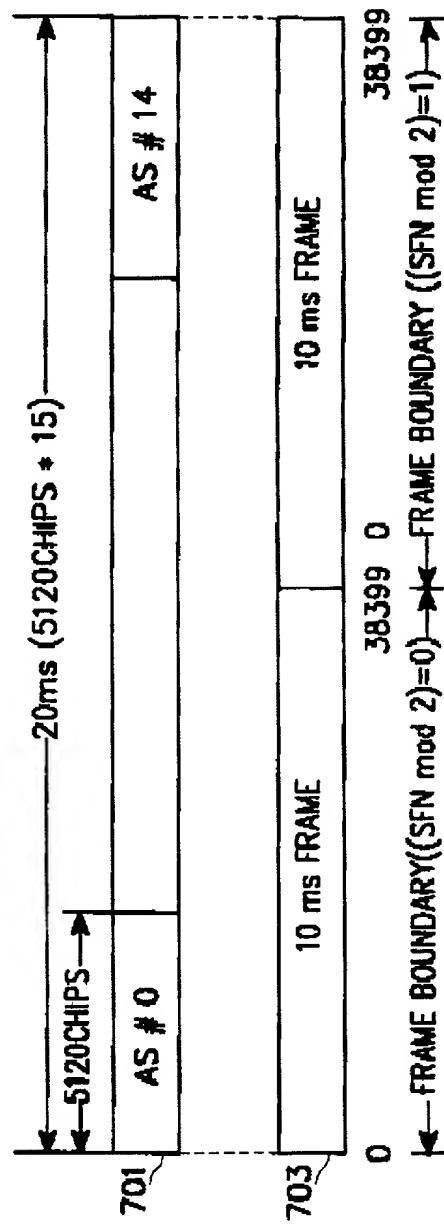


FIG. 6

FIG. 7



801	803	805	807
AP_SCRAMBLING CODE #A	CDP_SCRAM- BLING_CODE #A	PCP_SCRAMBLING_CODE #B	MESSAGE_SCRAMBLING_CODE #B
0 4095 4096	8191 0	20479	38400 CHIPS

FIG. 8A

811	813	815	817
AP_SCRAMBLING CODE #A	CDP_SCRAM- BLING_CODE #A	PCP_SCRAMBLING_CODE #F	MESSAGE_SCRAMBLING_CODE #G
0 4095 4096	8191 0	20479 0	38399

FIG. 8B

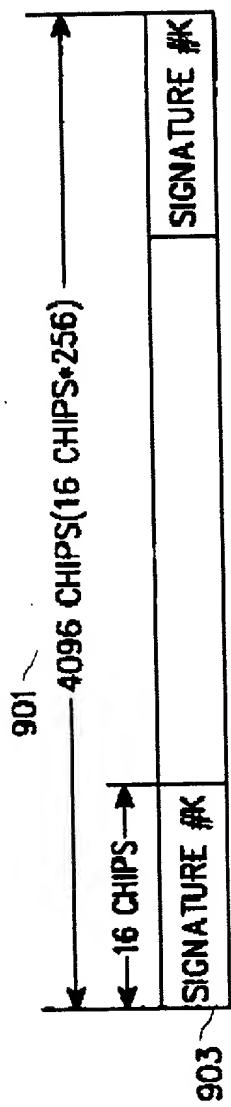


FIG. 9A

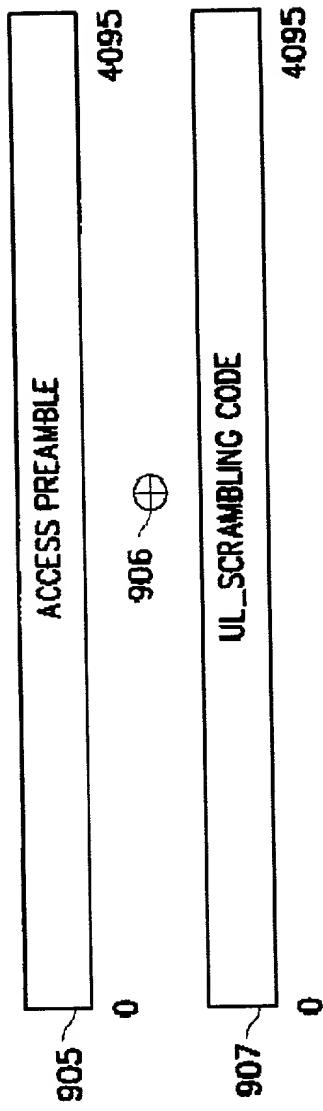


FIG. 9B

FIG. 10A

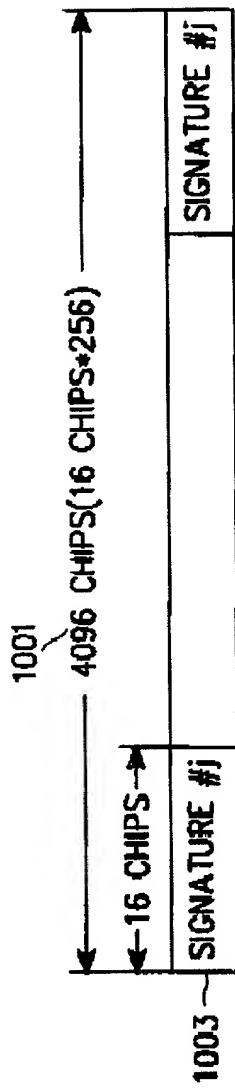


FIG. 10B

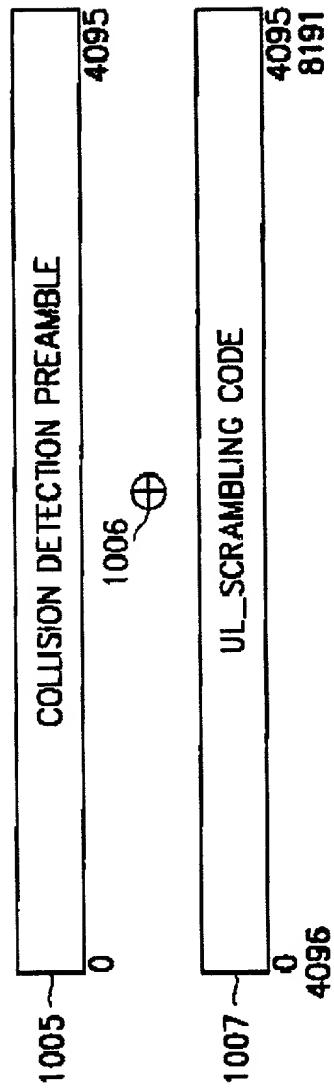


FIG. 11A

FIG. 11B

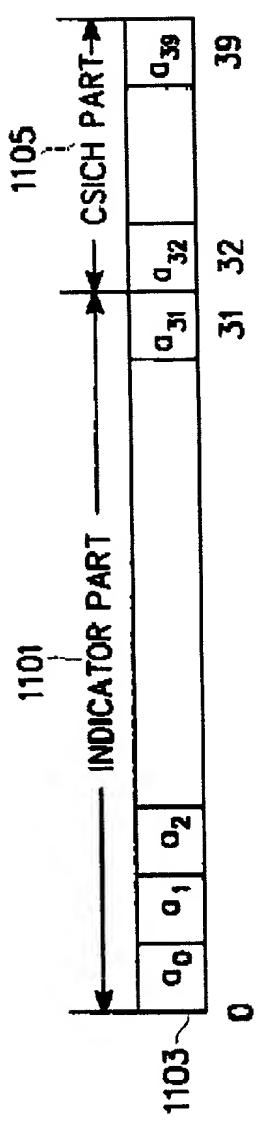
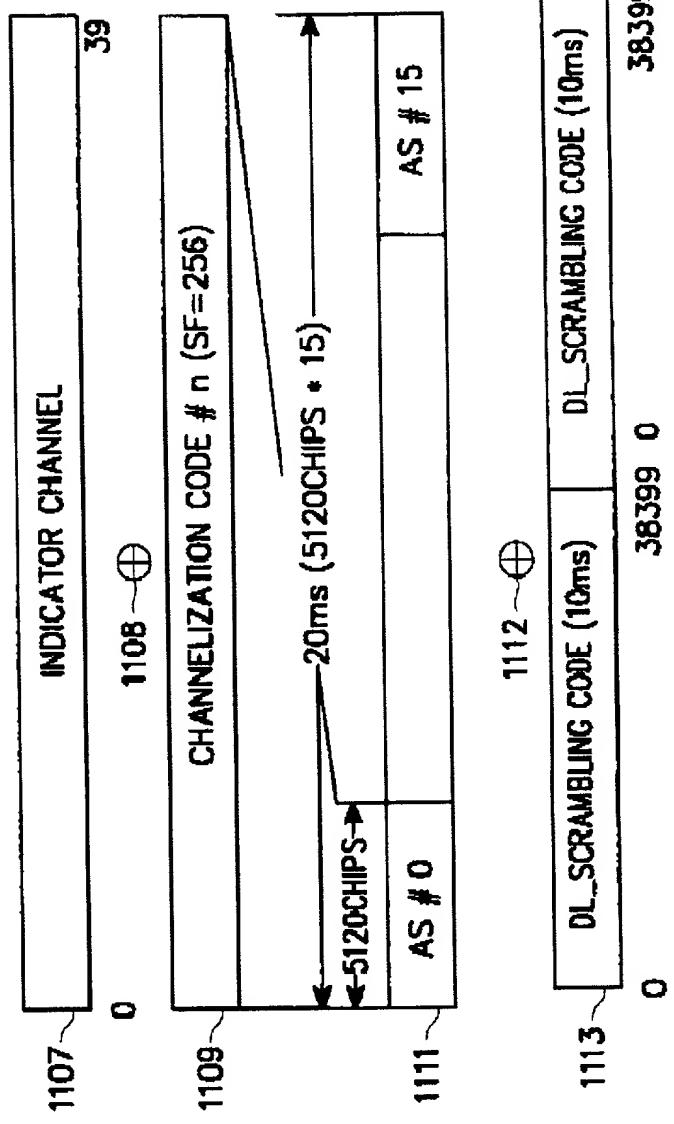


FIG. 11B

FIG. 11C



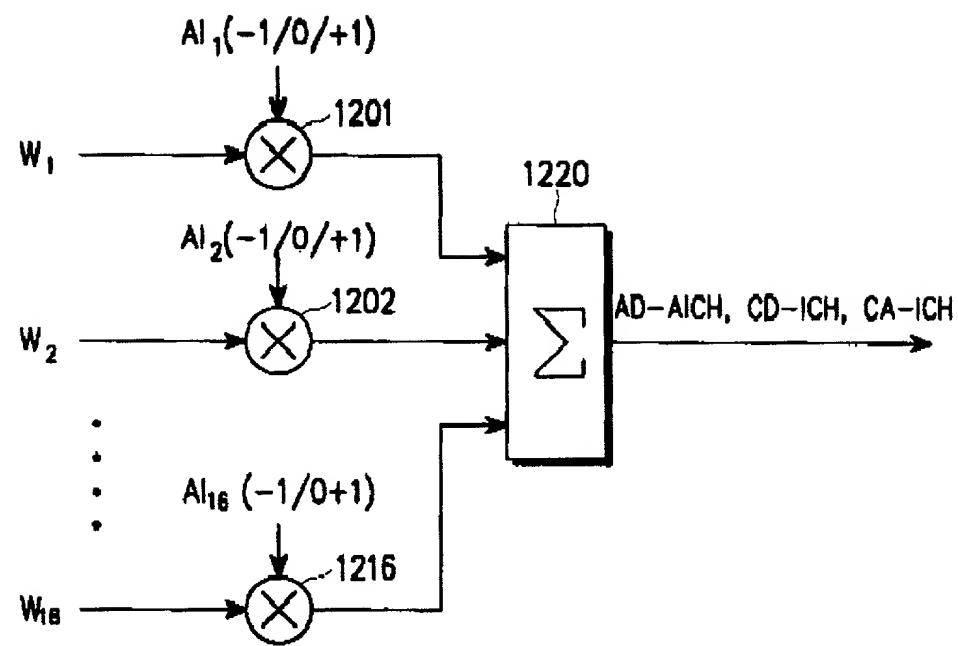


FIG. 12

FIG. 13A

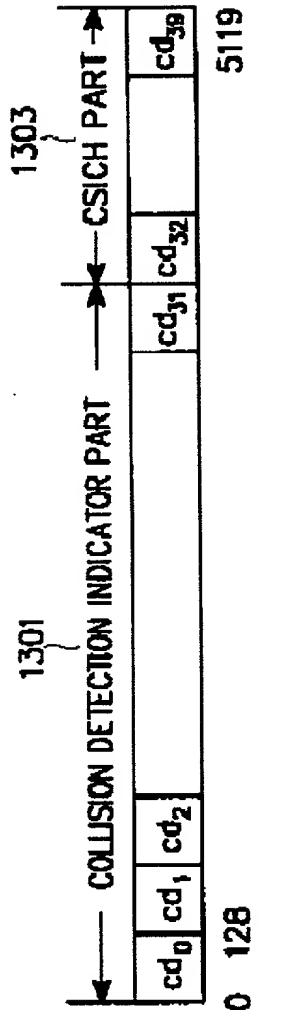
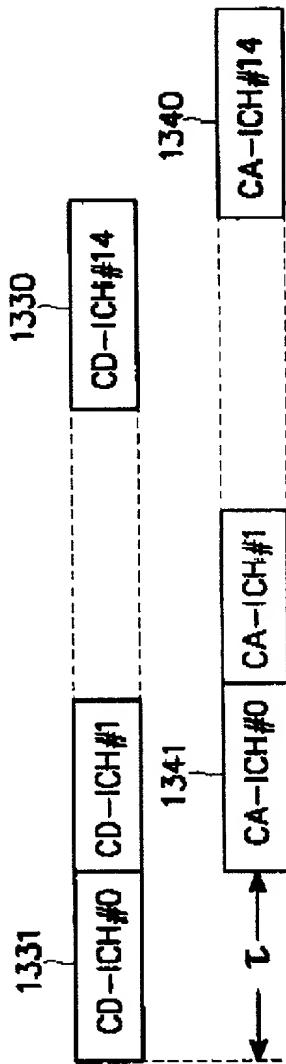
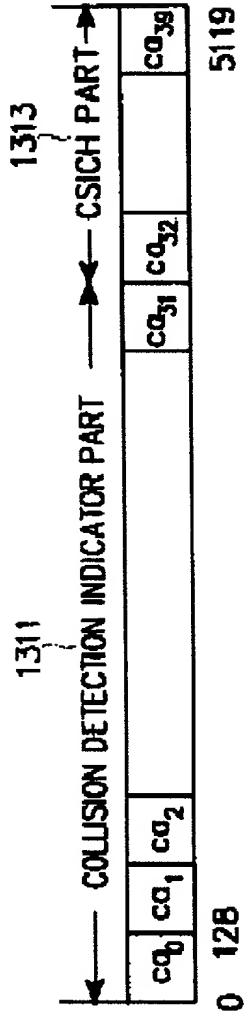


FIG. 13B



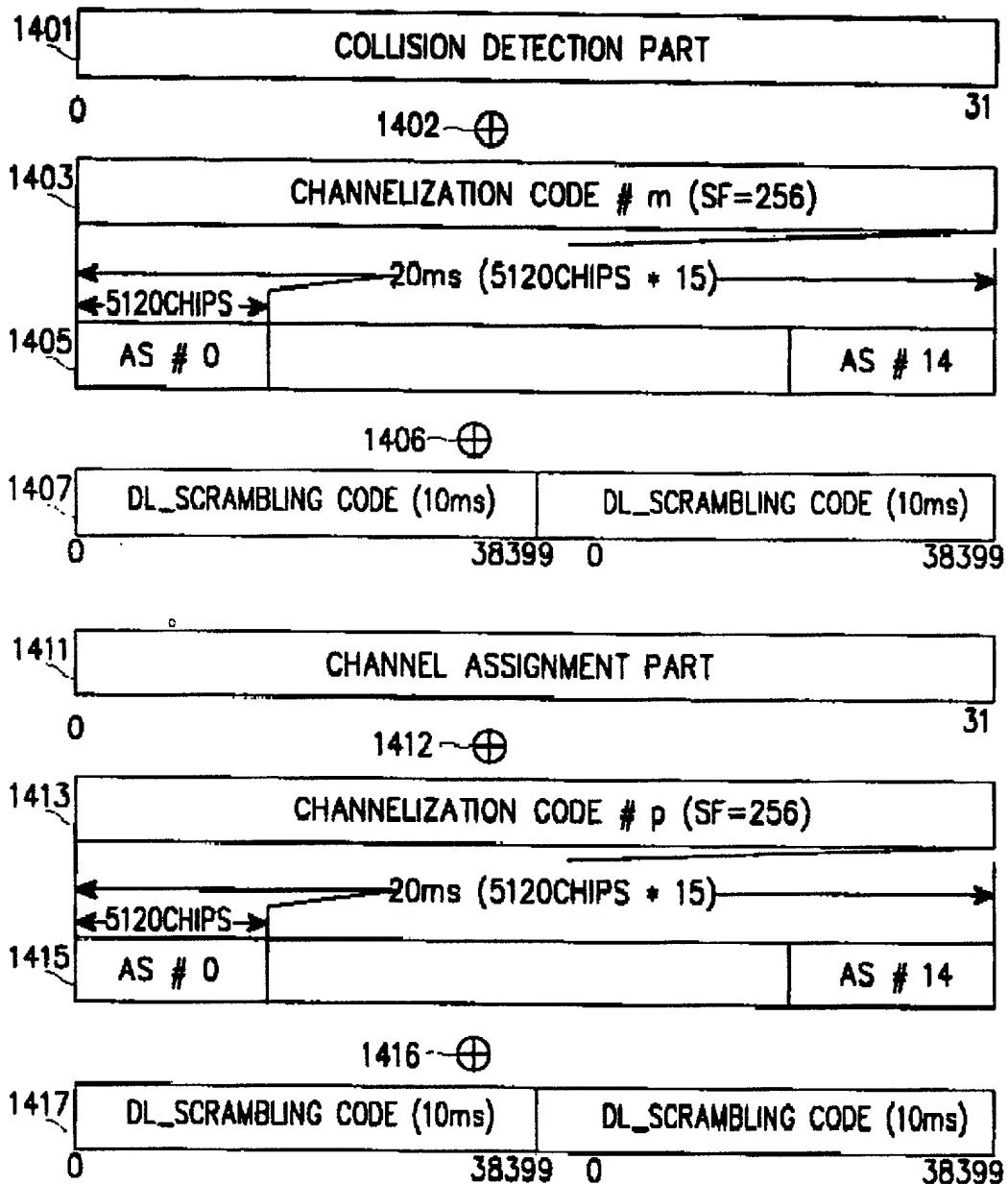


FIG. 14

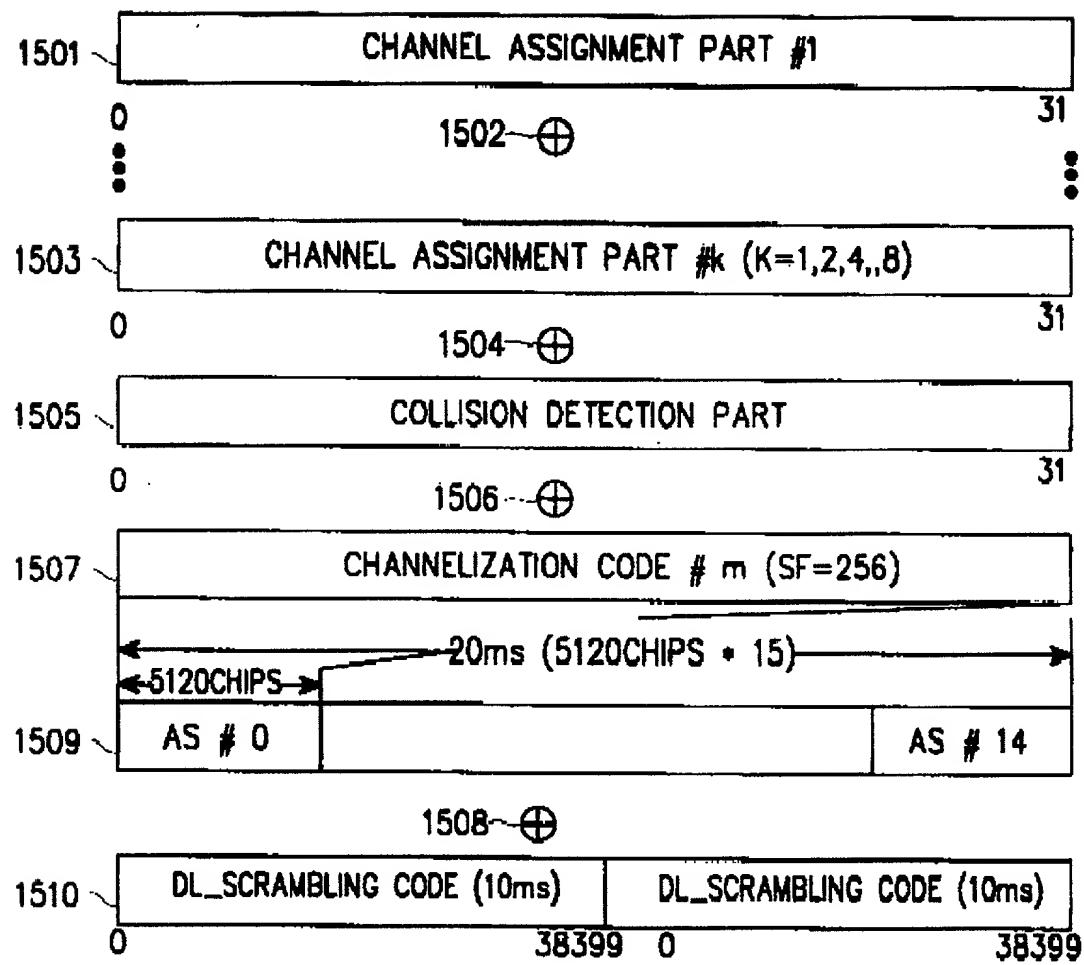


FIG. 15

FIG. 16

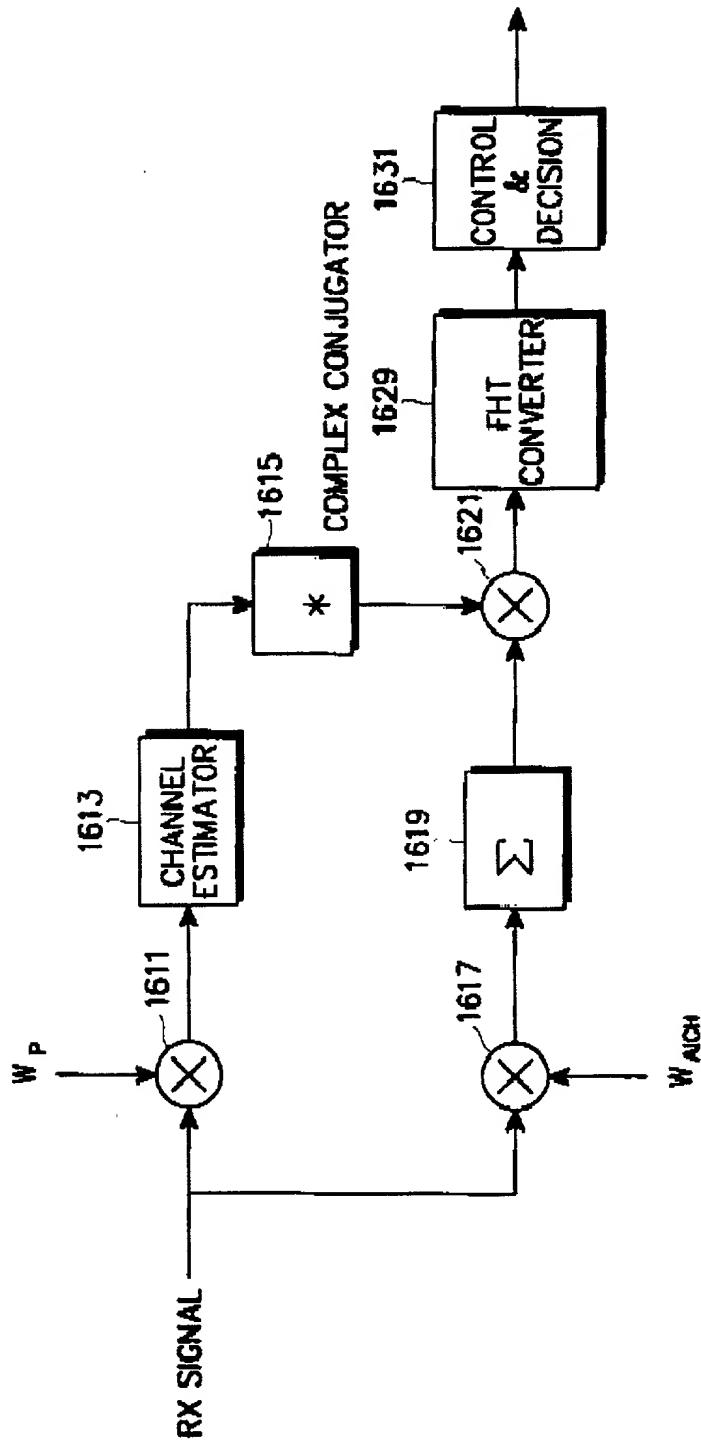
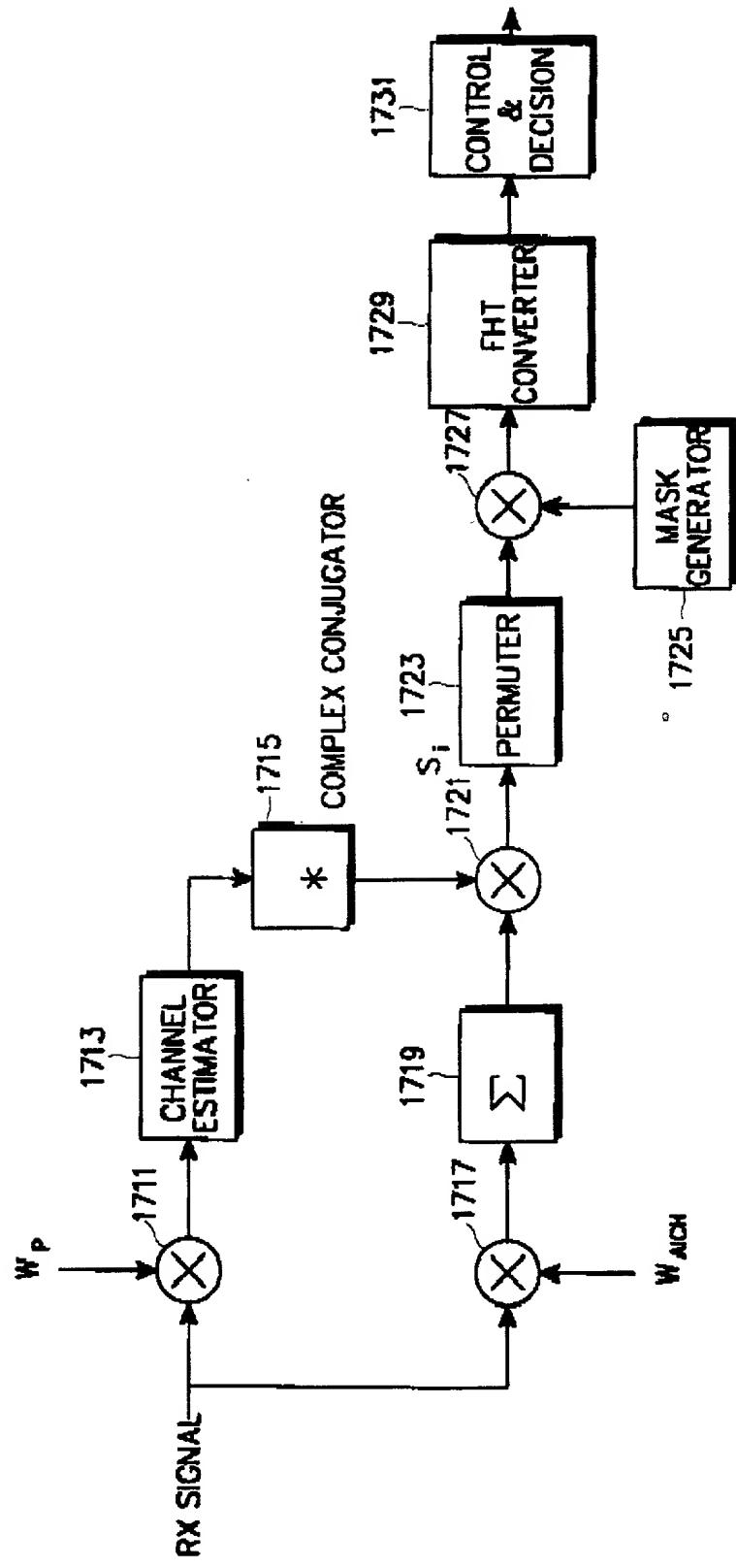


FIG. 17



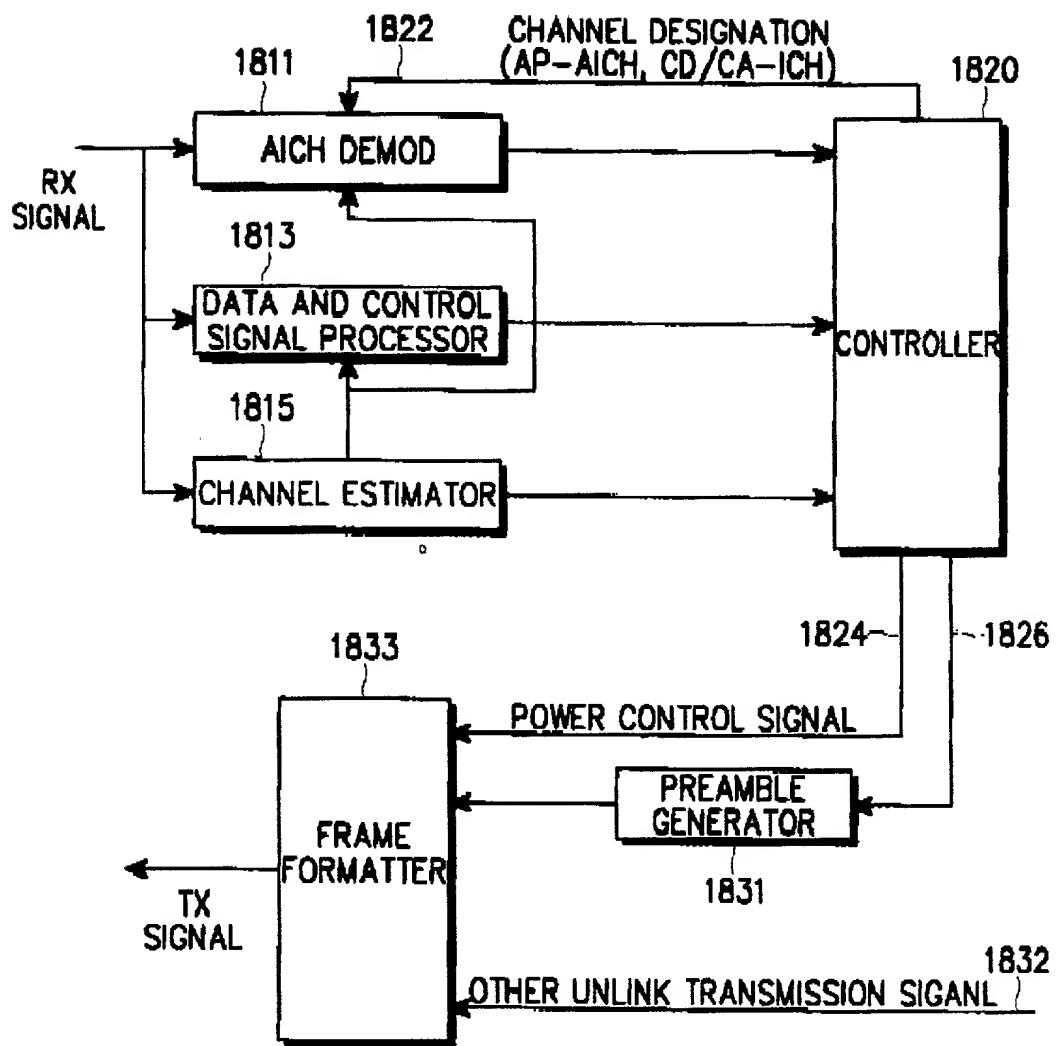


FIG. 18

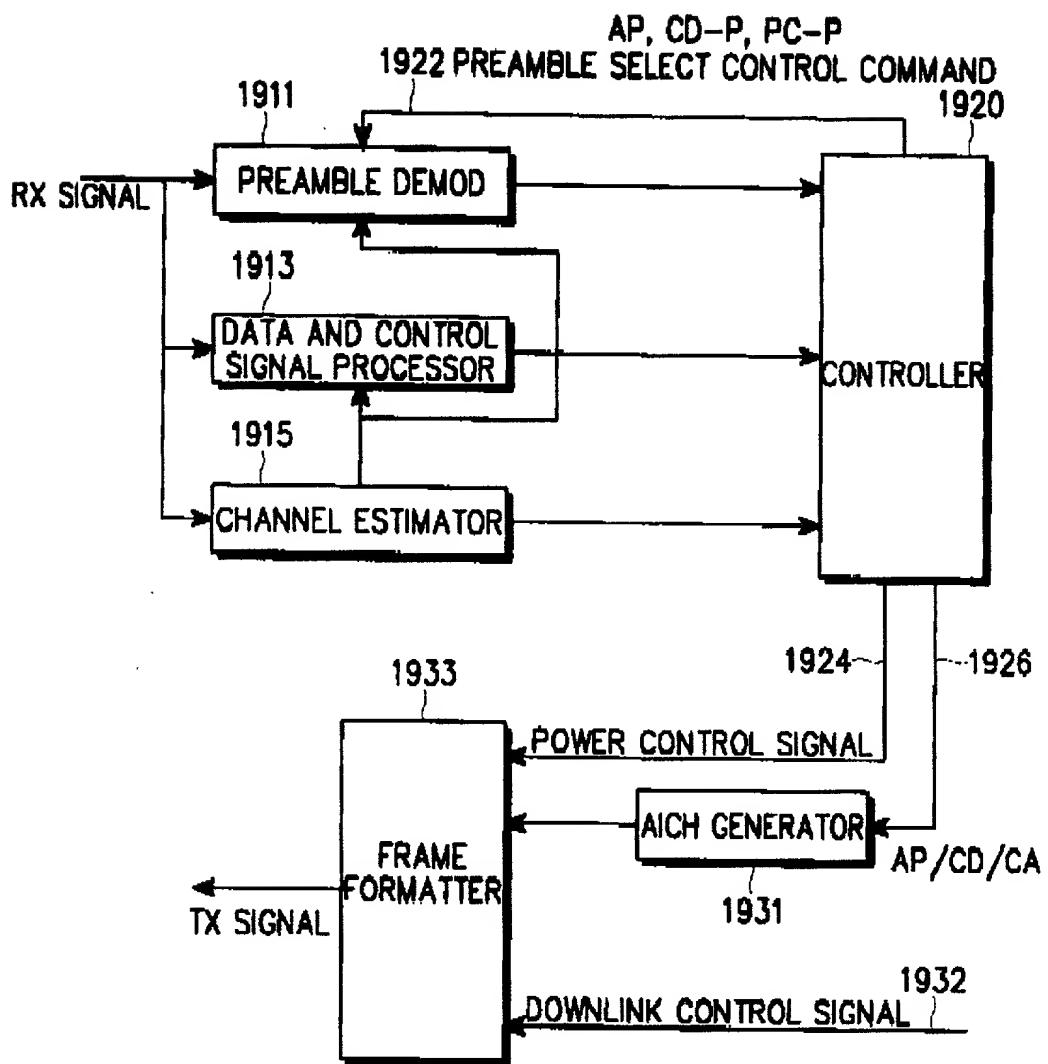


FIG. 19

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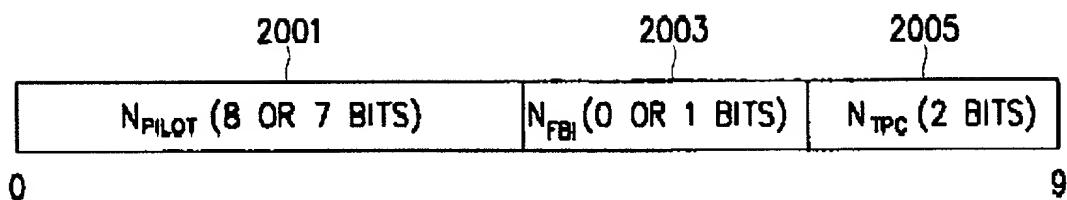


FIG. 20

FIG. 21

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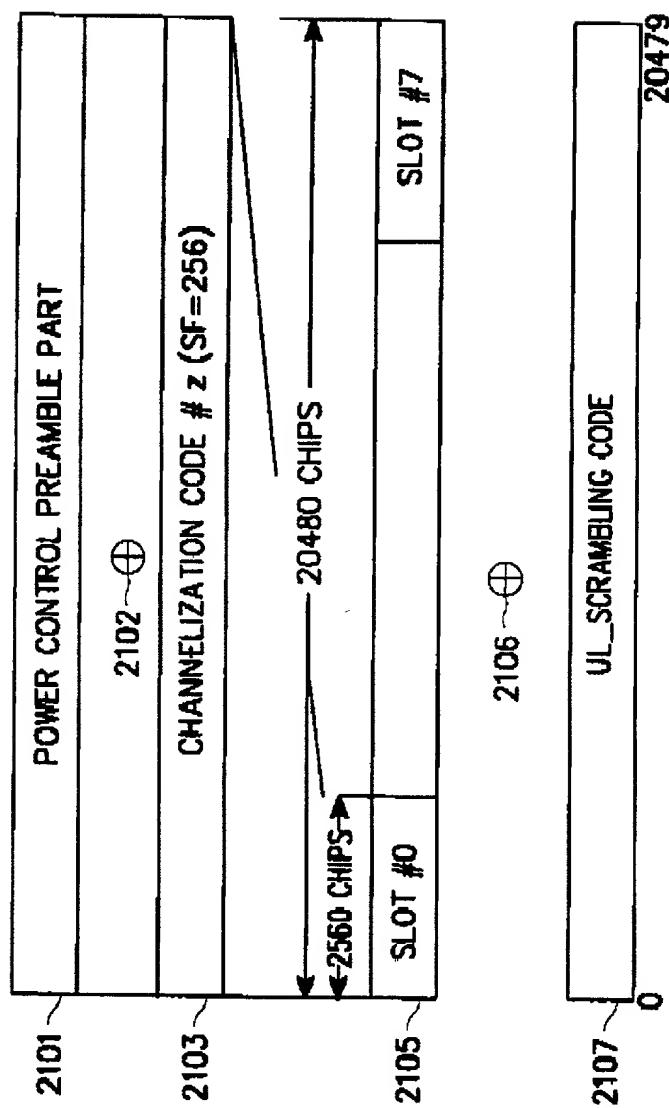


FIG. 22A

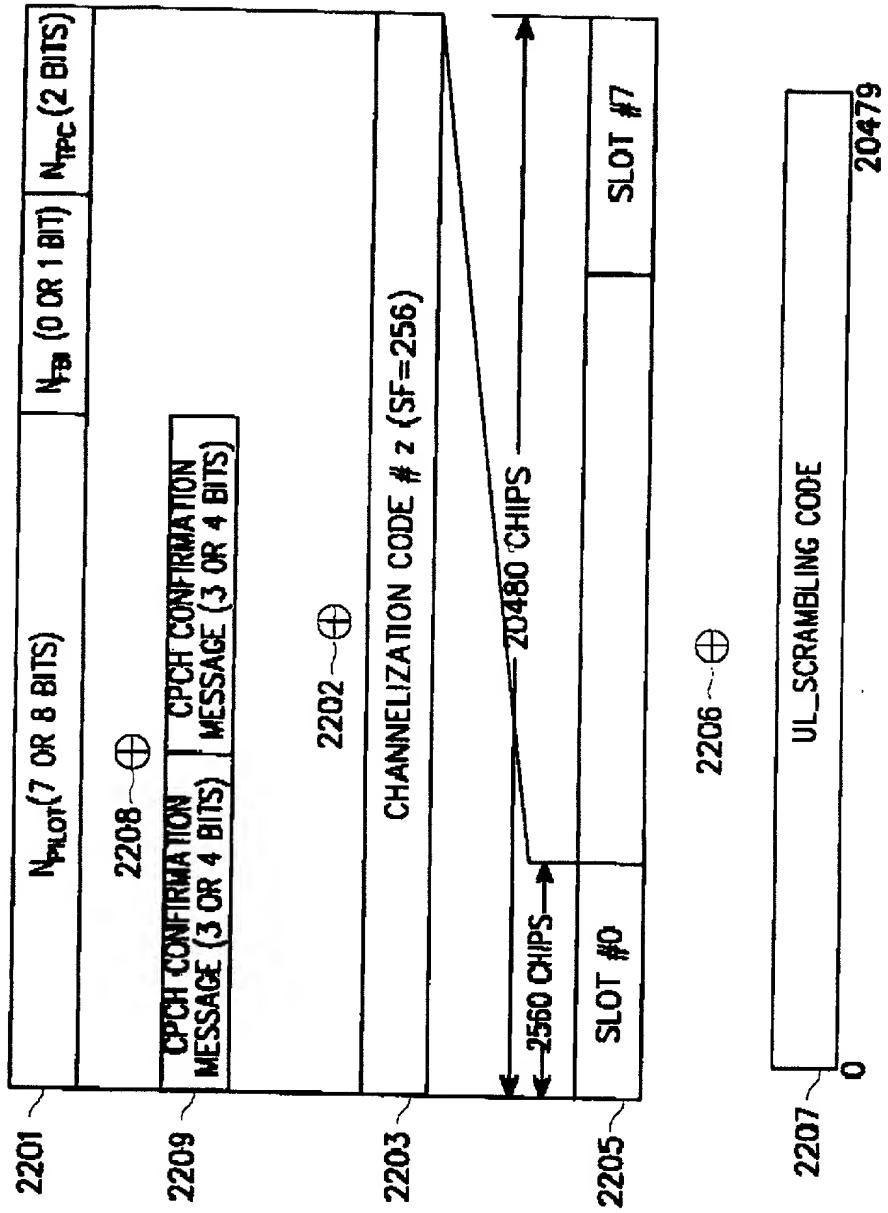


FIG. 22B

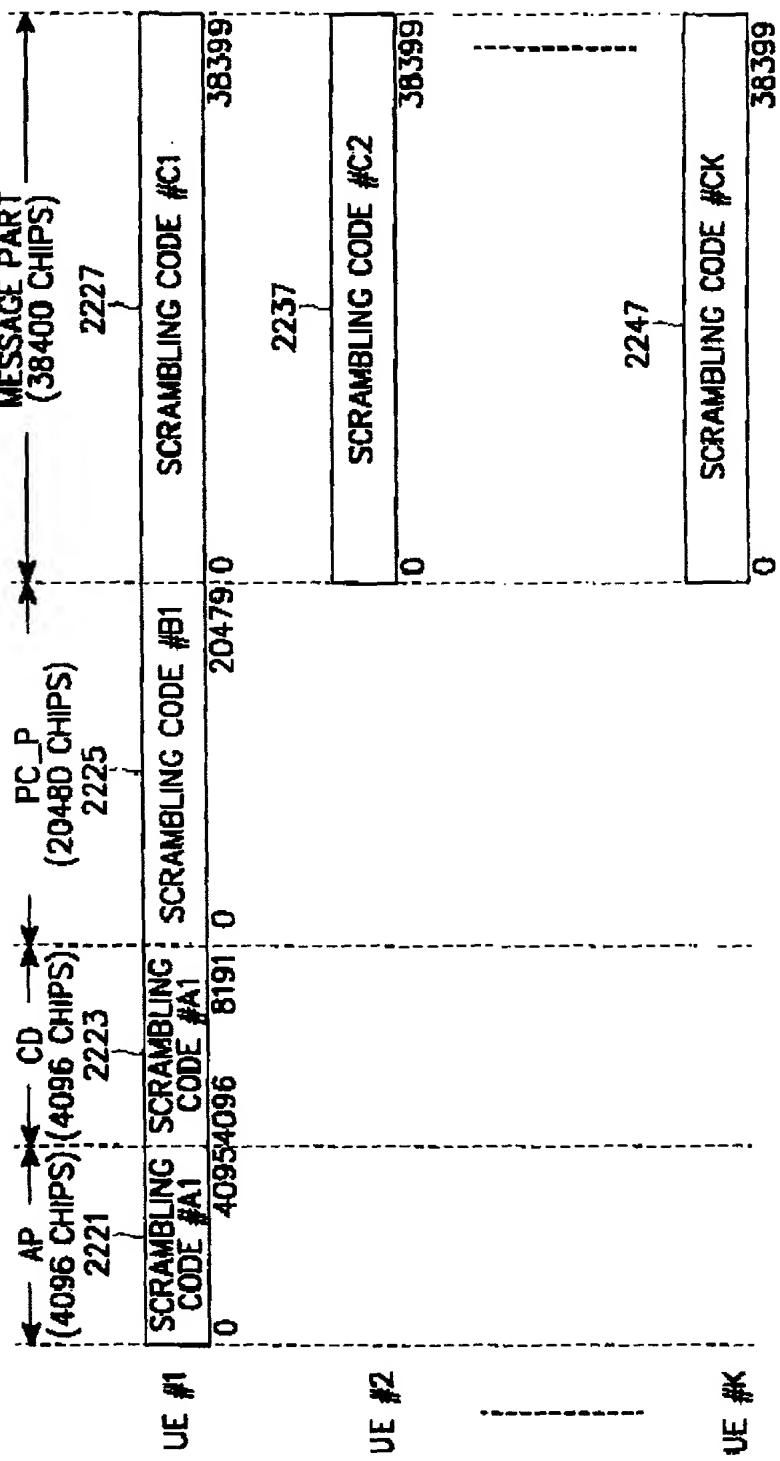


FIG. 23

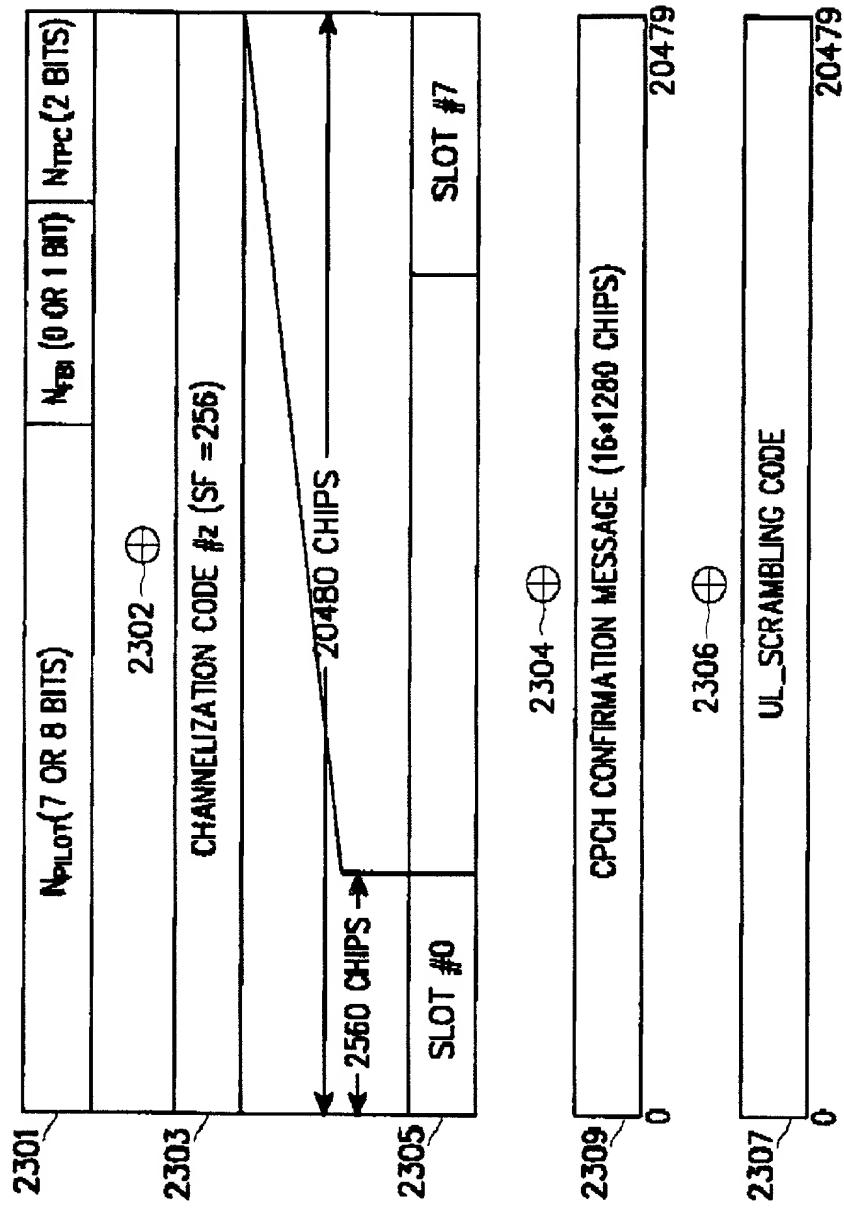


FIG. 24A

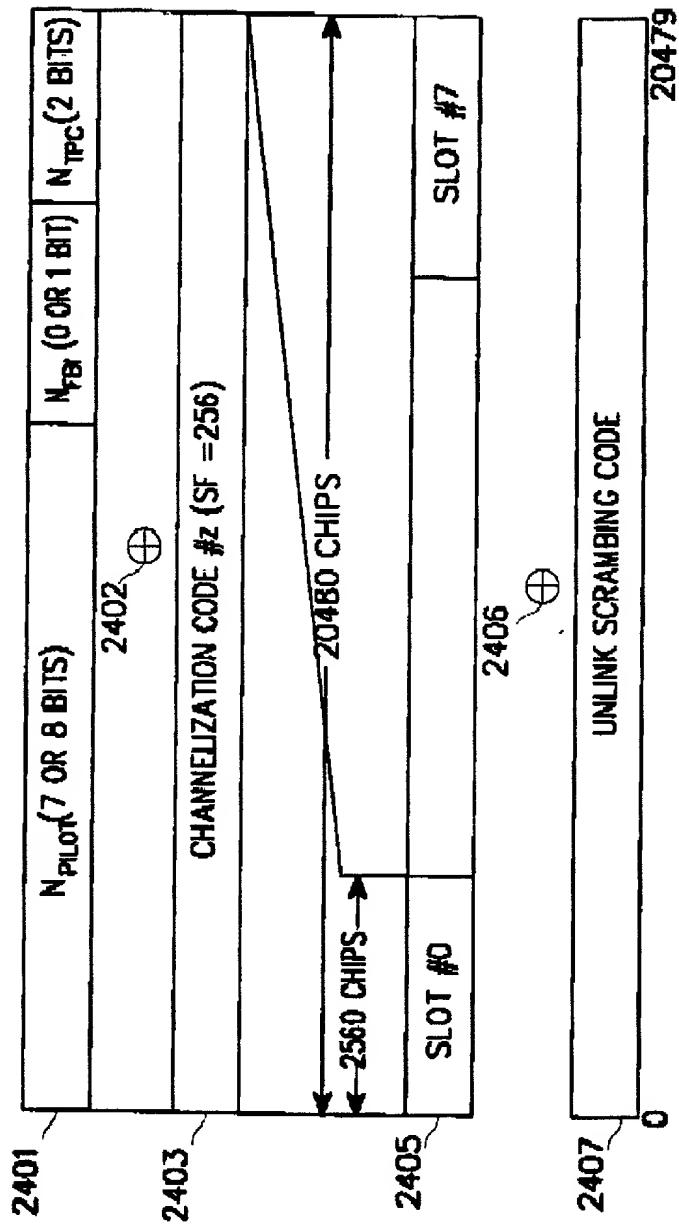


FIG. 24B

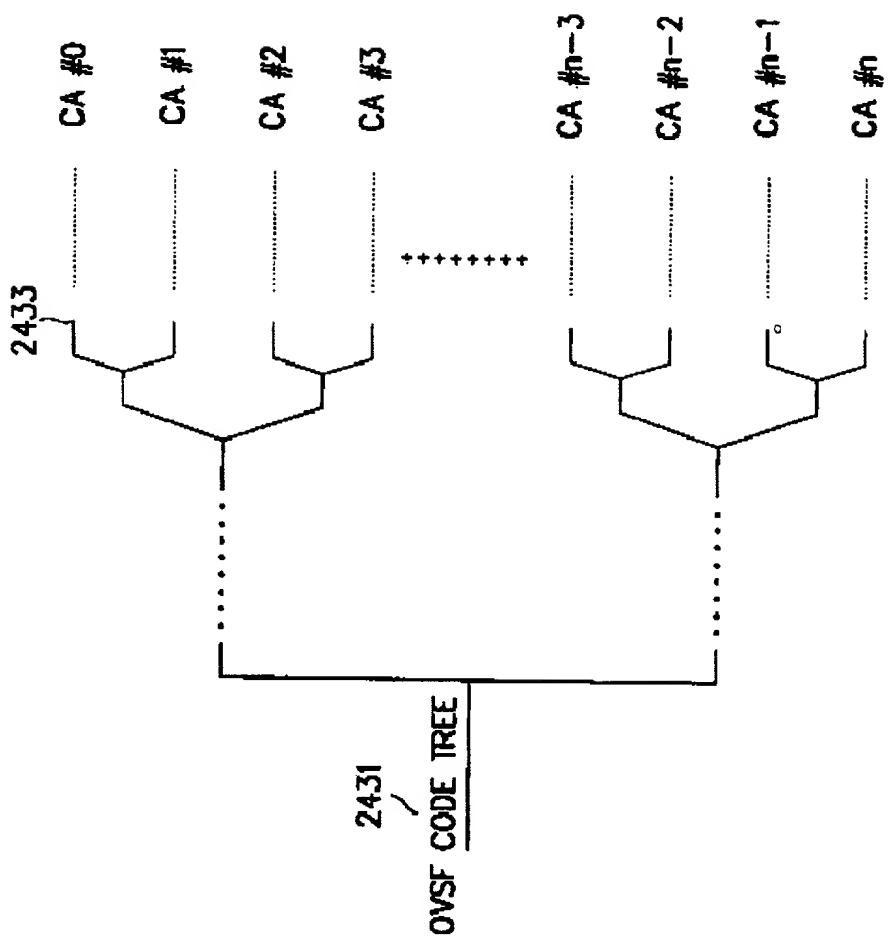


FIG. 25A

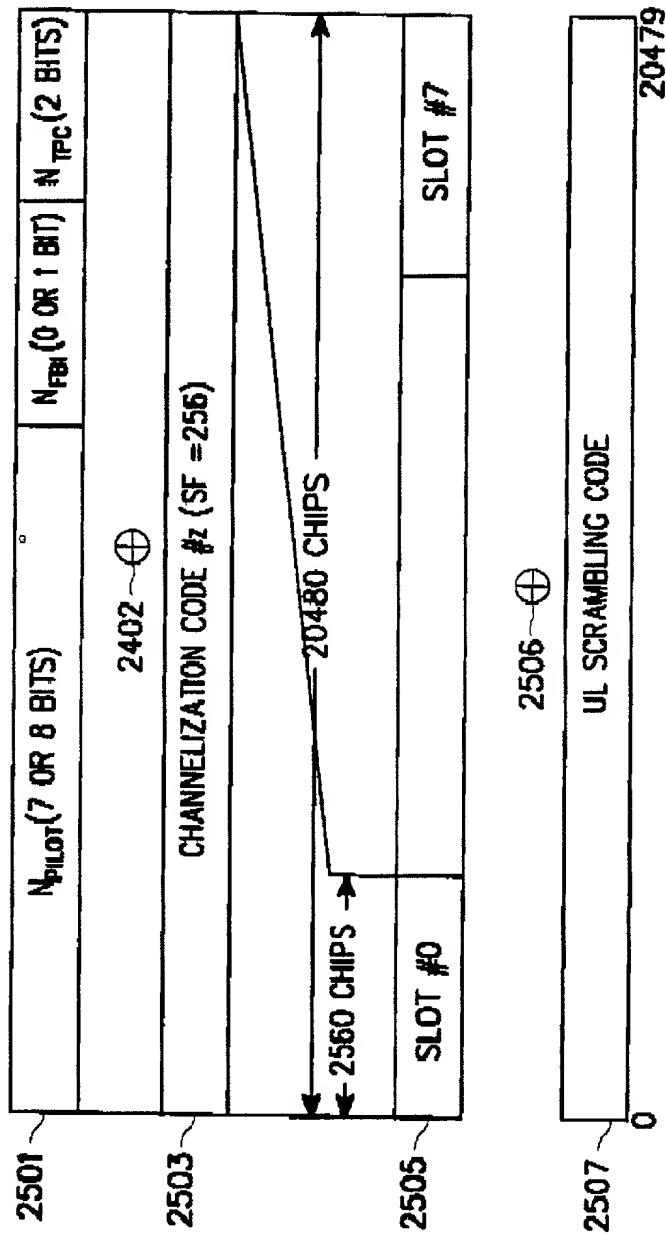
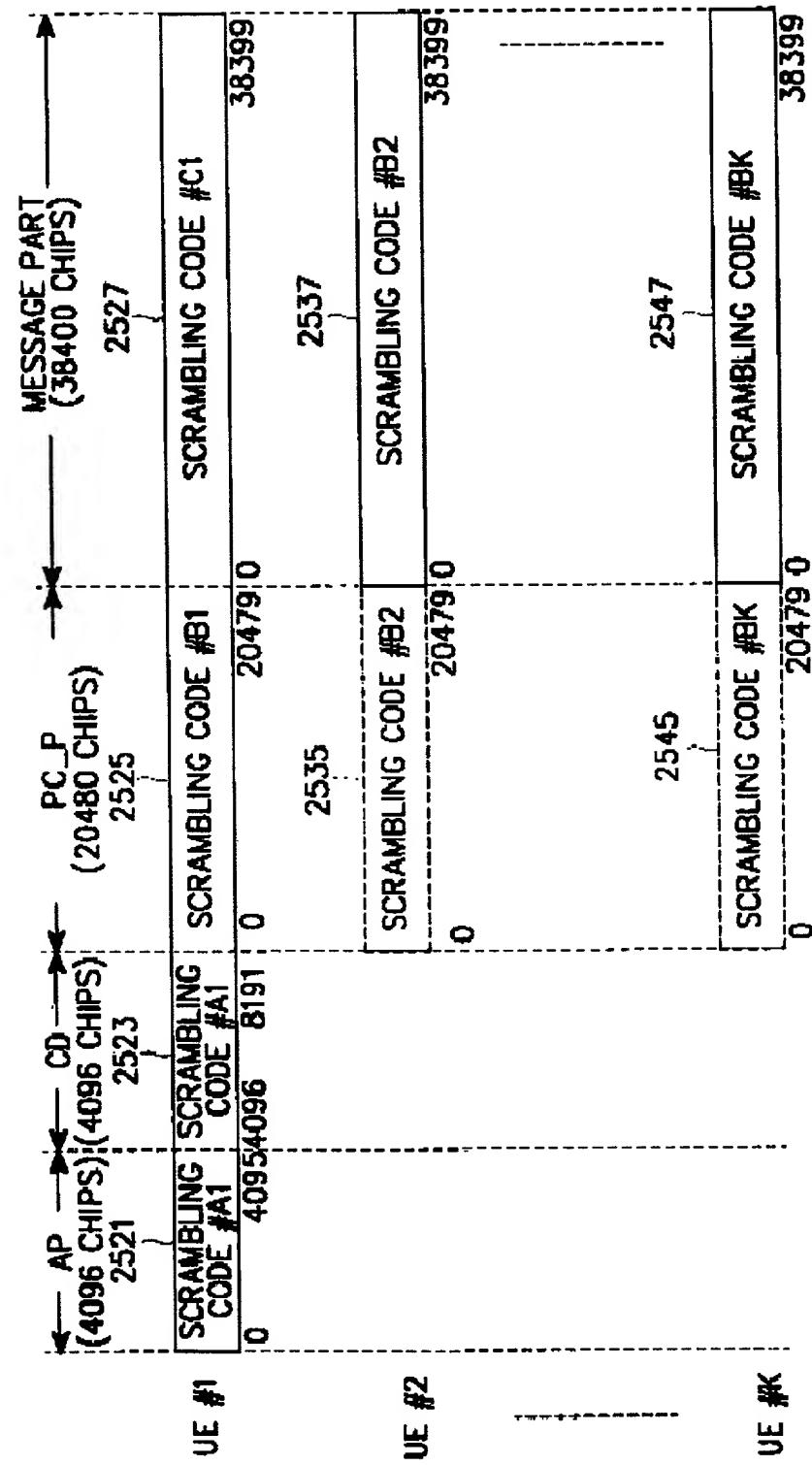


FIG. 25B



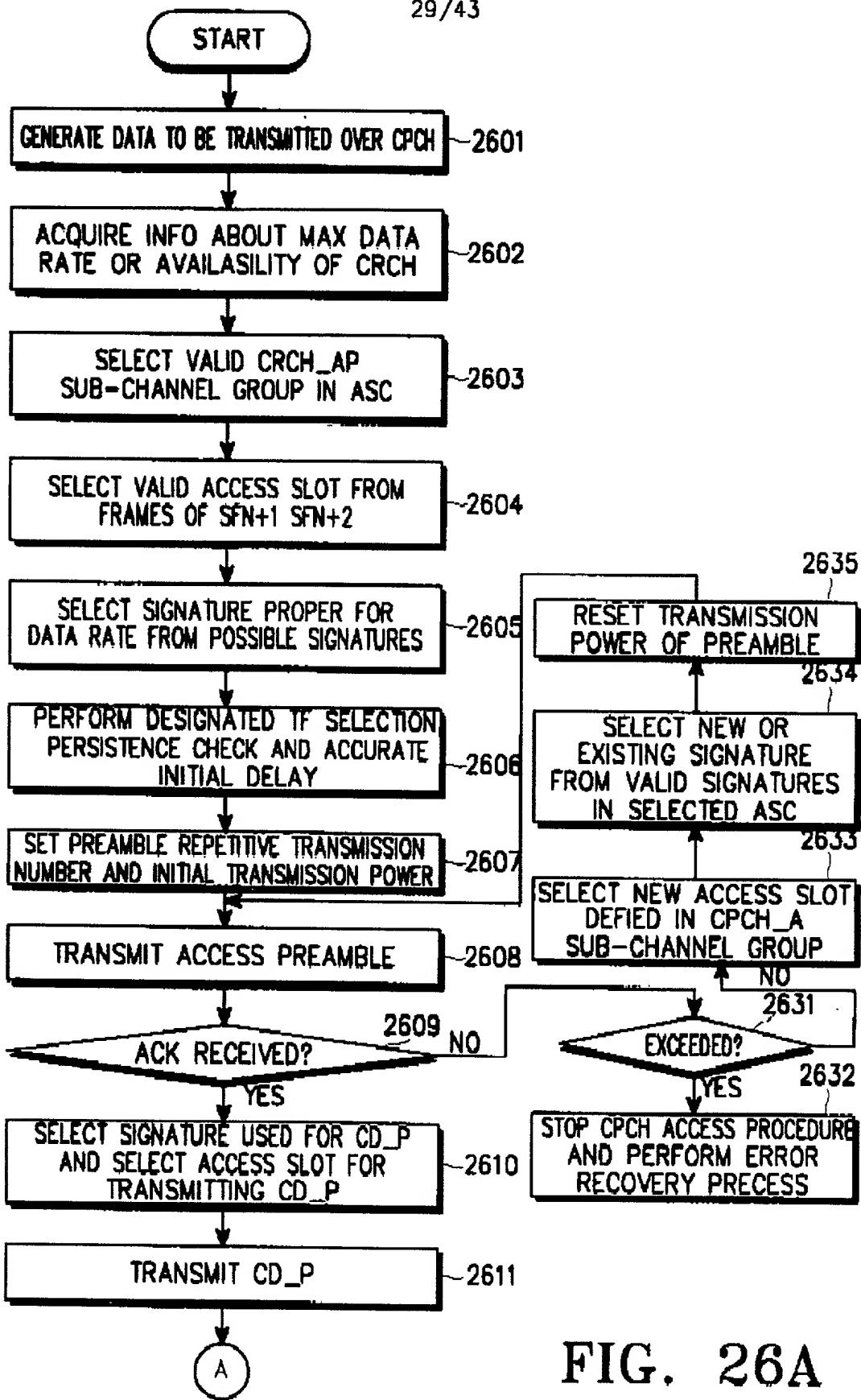


FIG. 26A

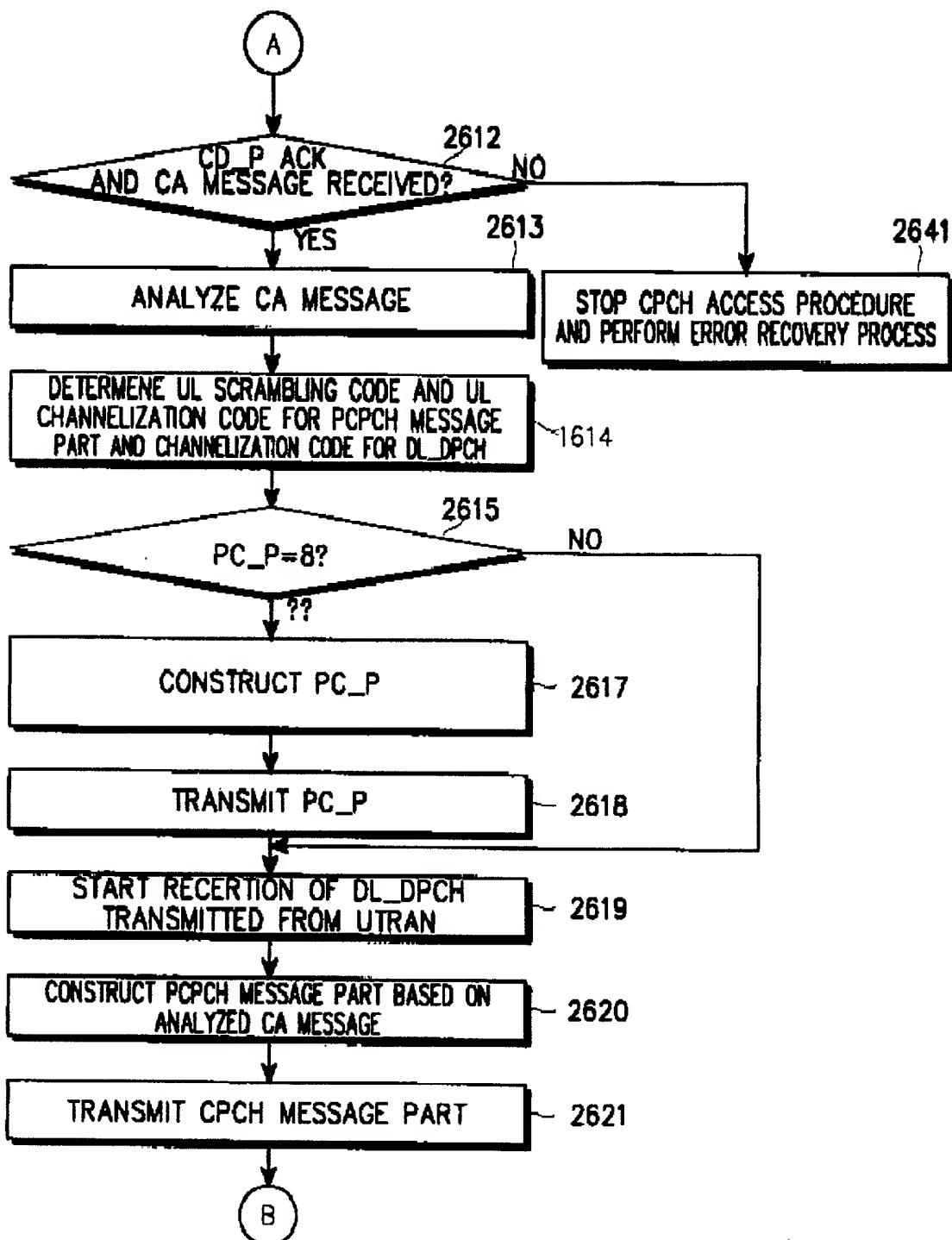


FIG. 26B

FLOWCHART OF CPCH USING CHANNEL ASSIGNMENT
(UE SIDE)

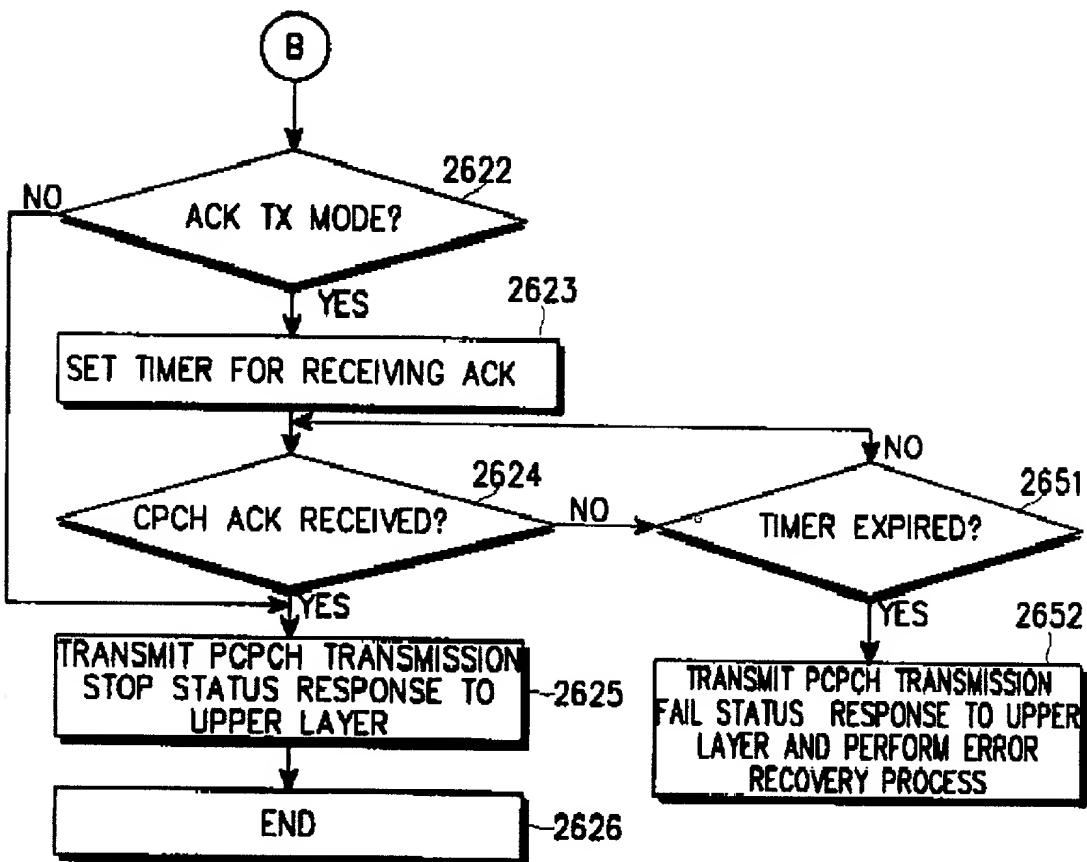


FIG. 26C

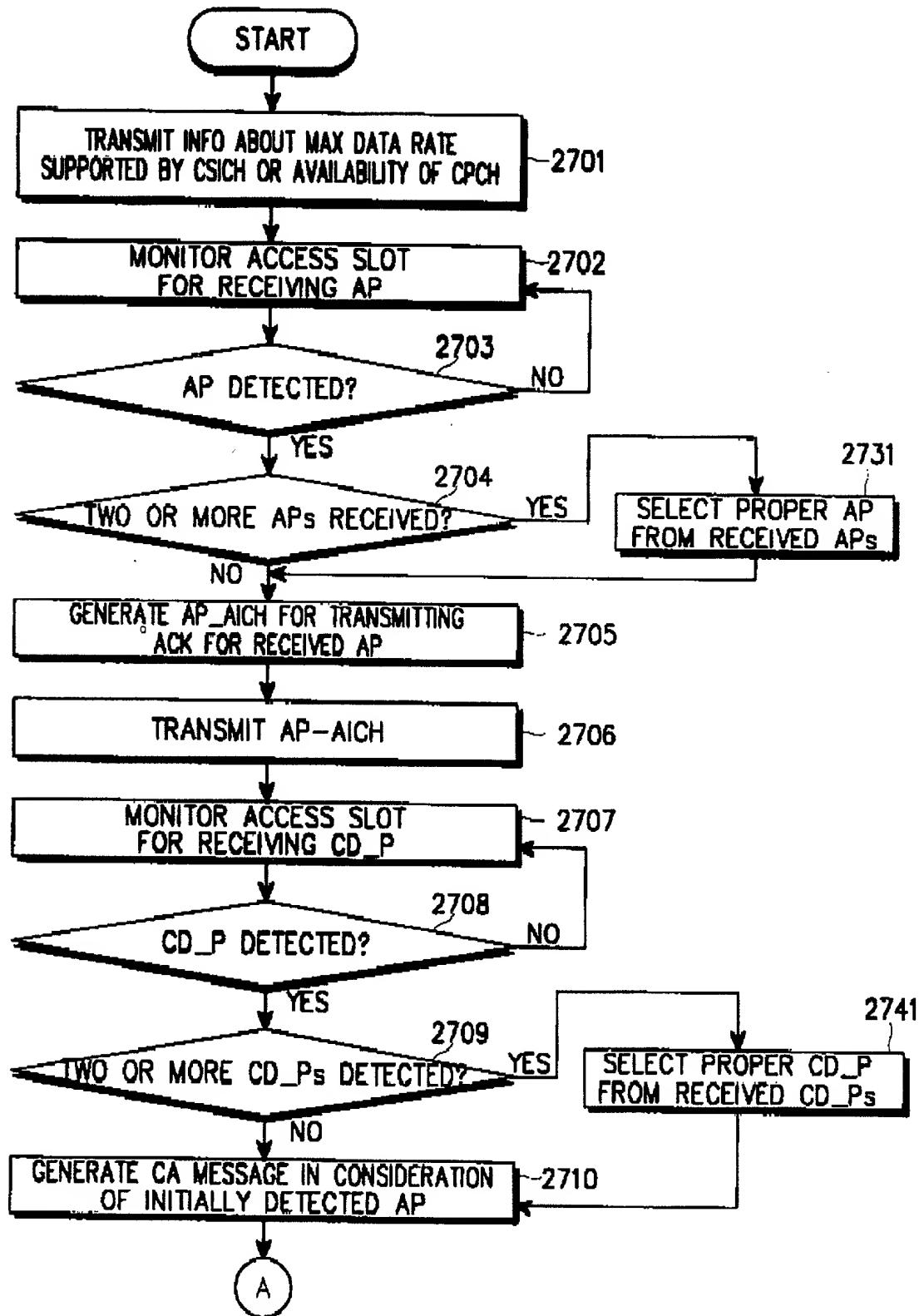


FIG. 27A

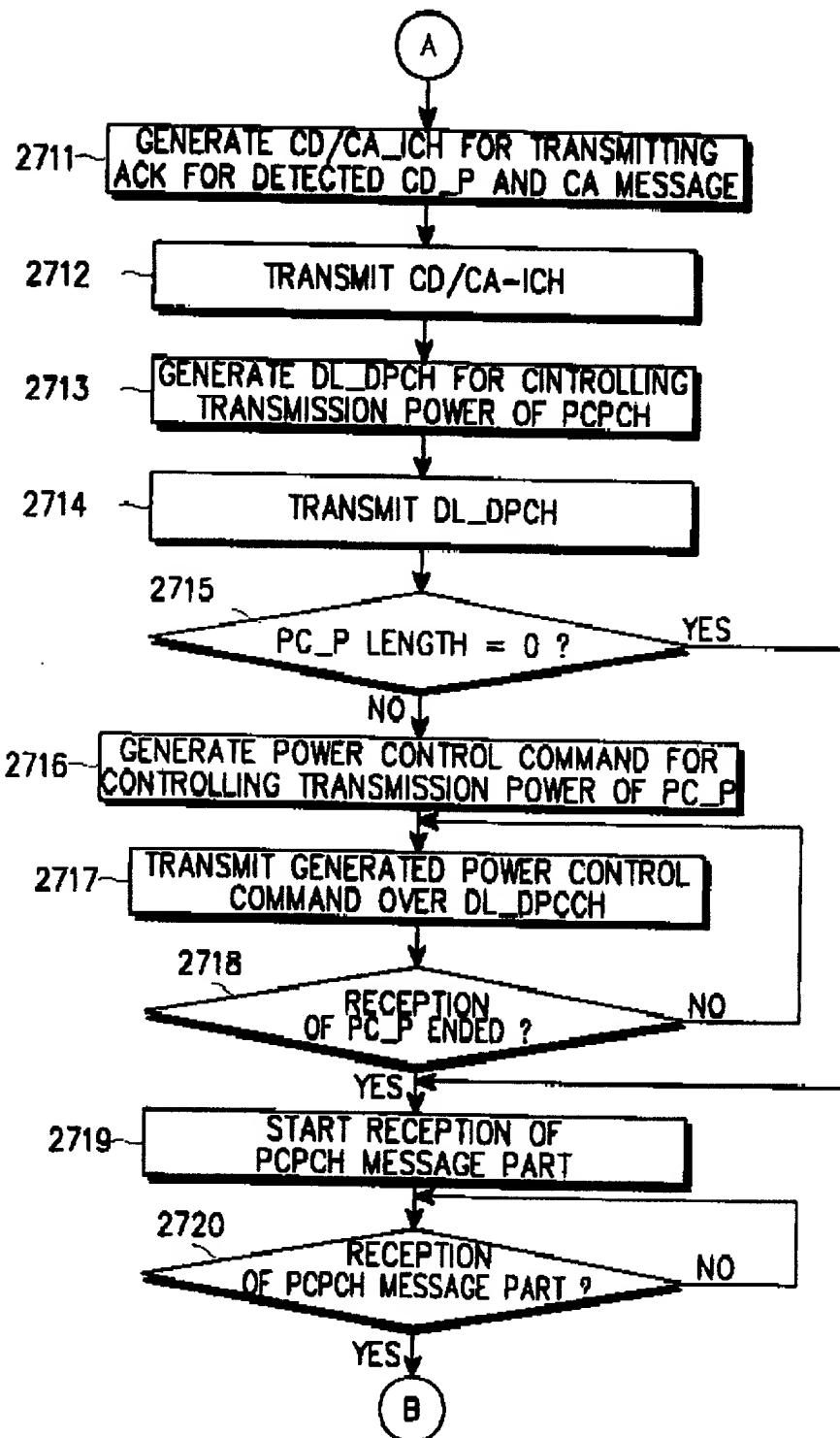


FIG. 27B

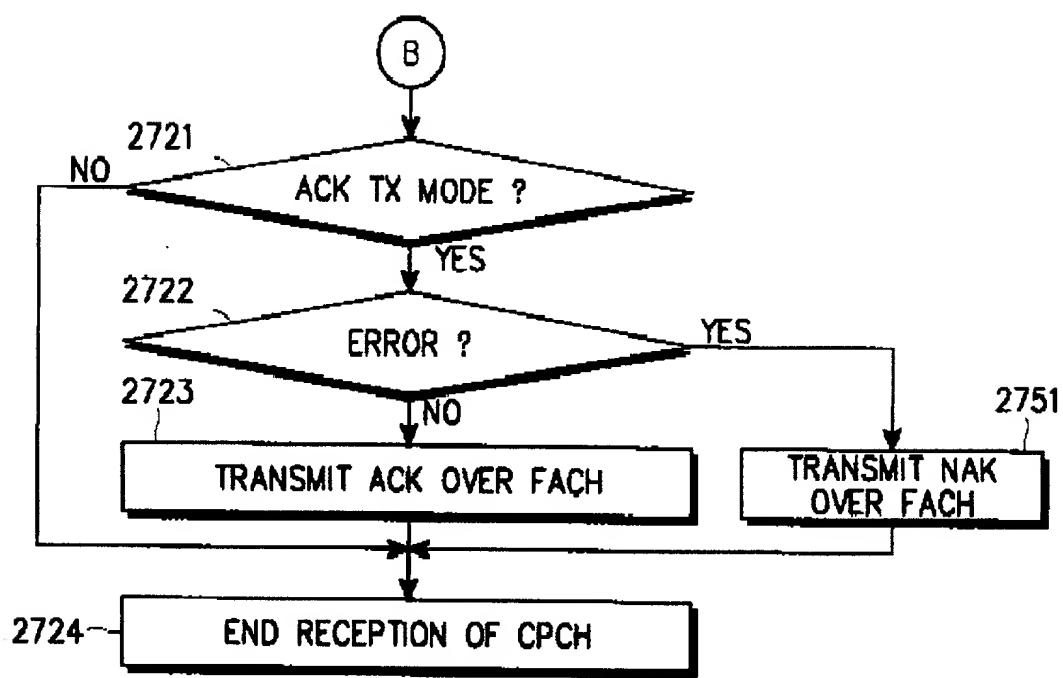
FLOWCHART OF CPCH USING CHANNEL ASSIGNMENT
(UTRAN SIDE)

FIG. 27C

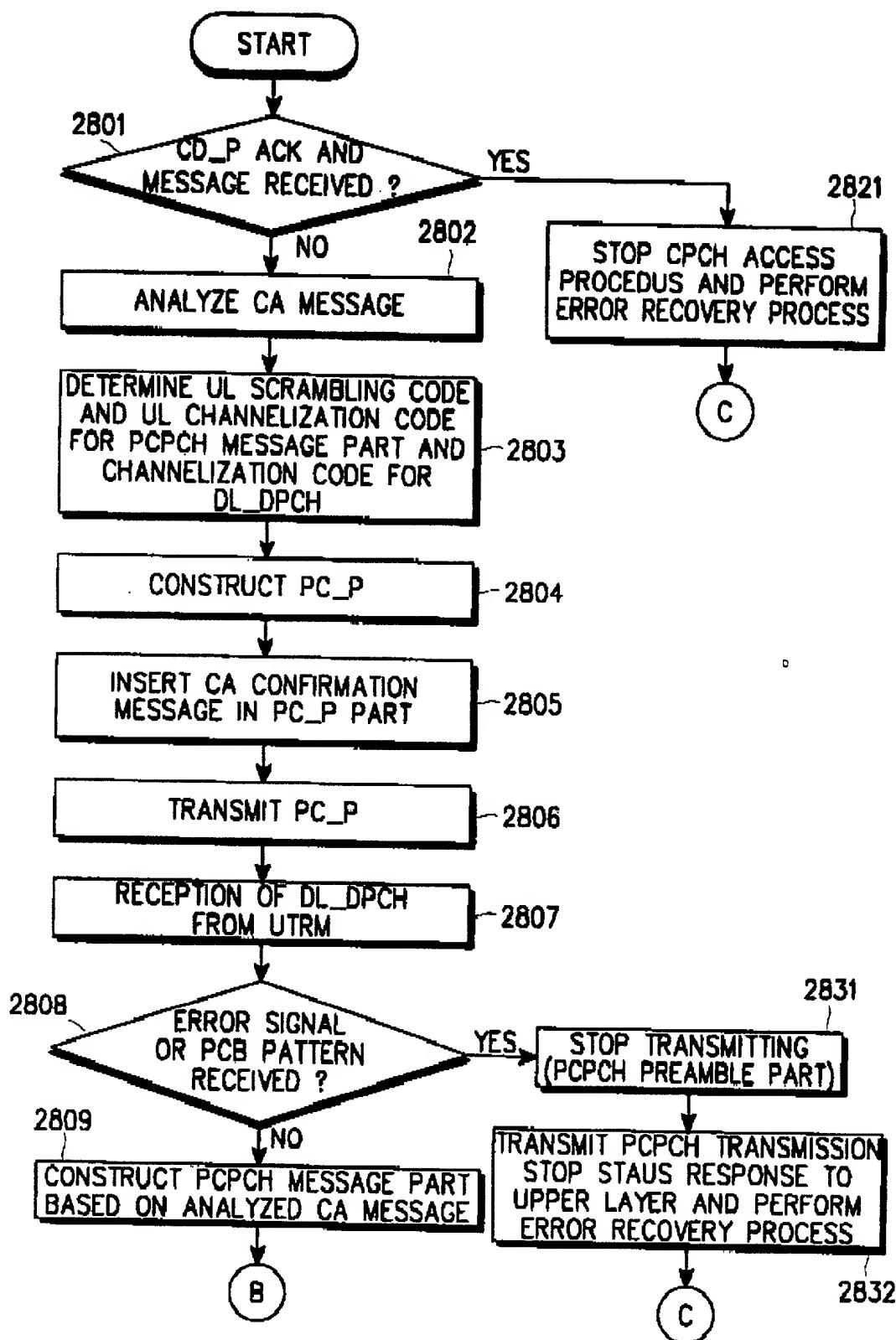


FIG. 28A

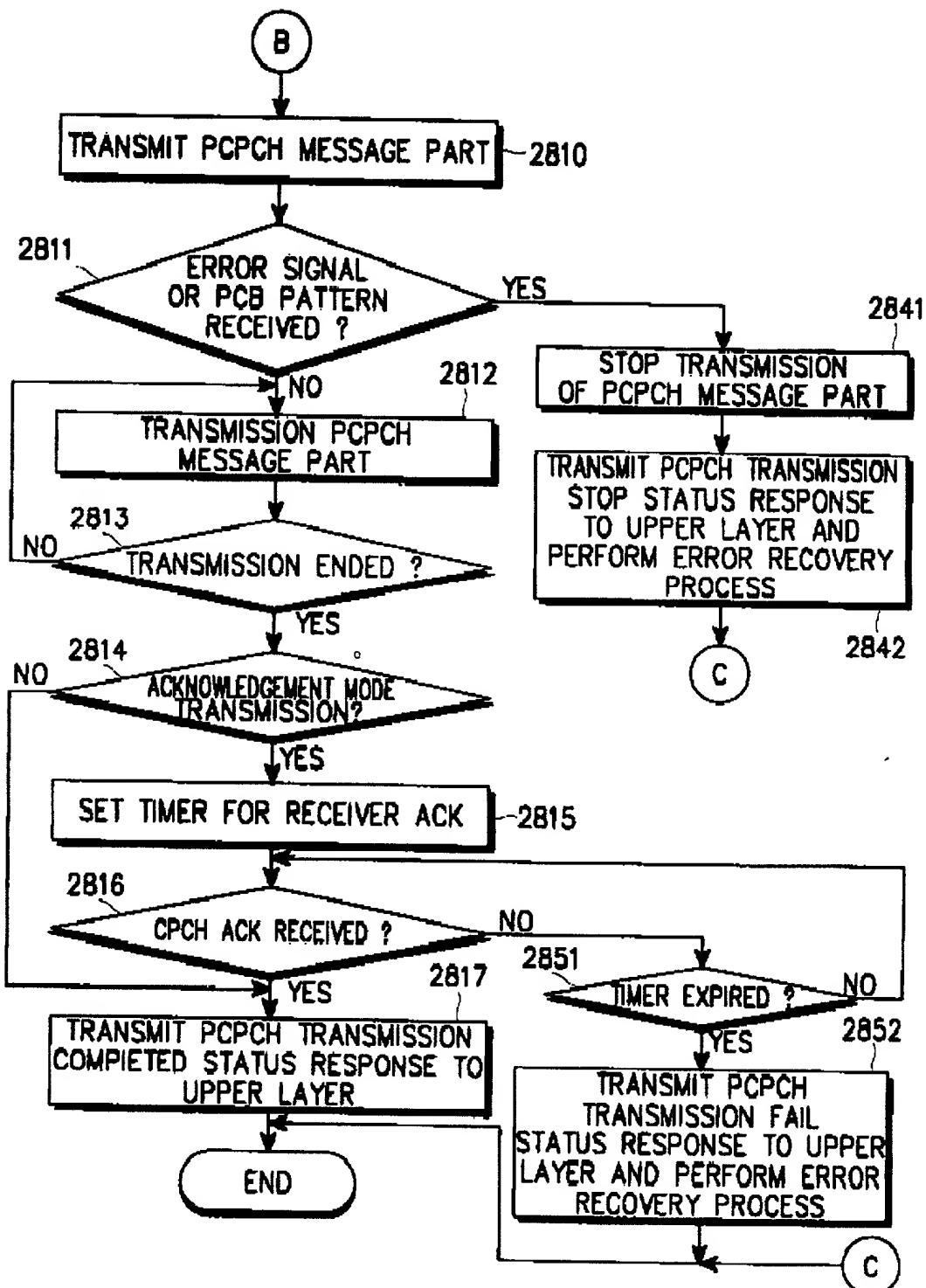


FIG. 28B

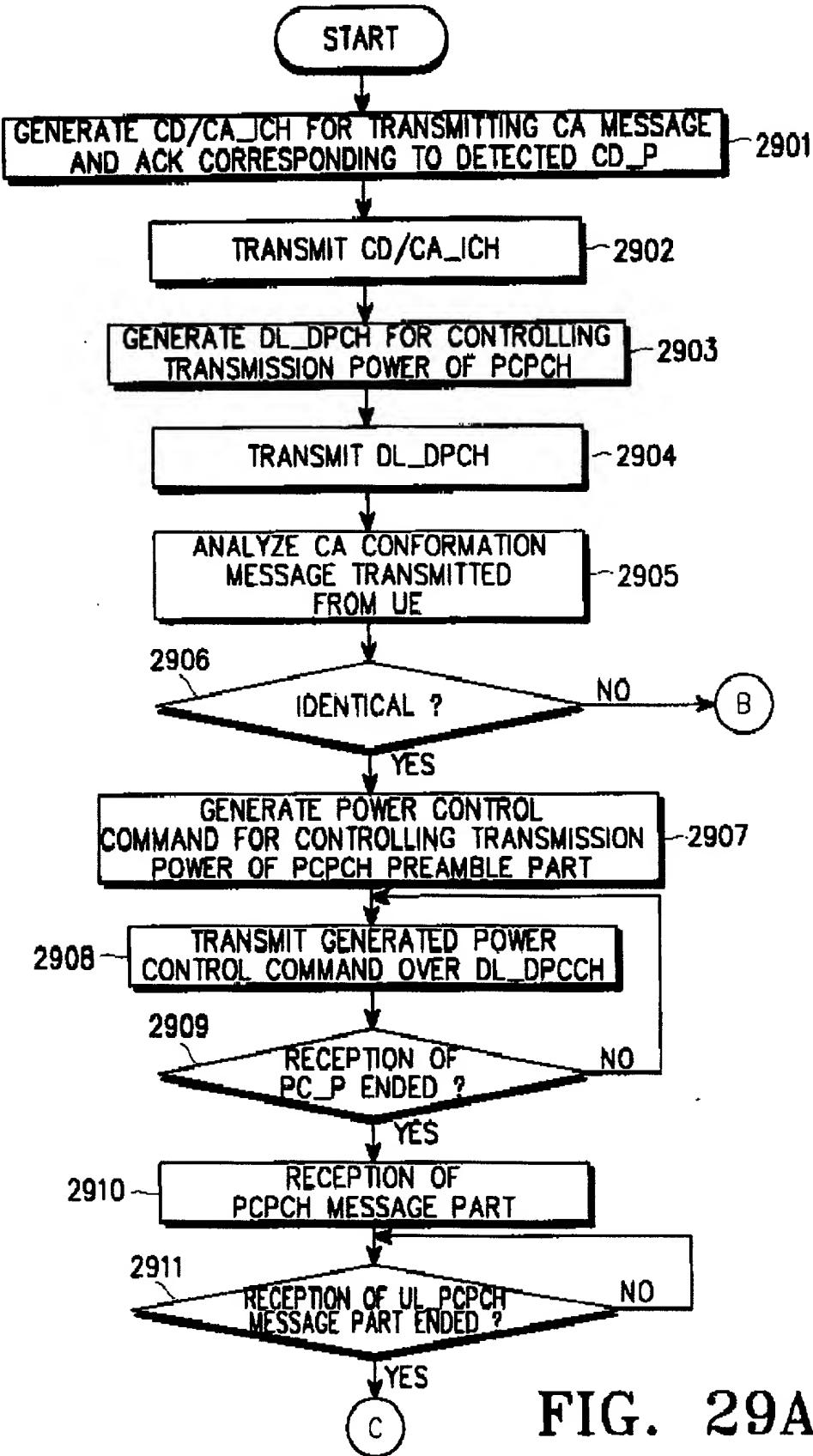


FIG. 29A

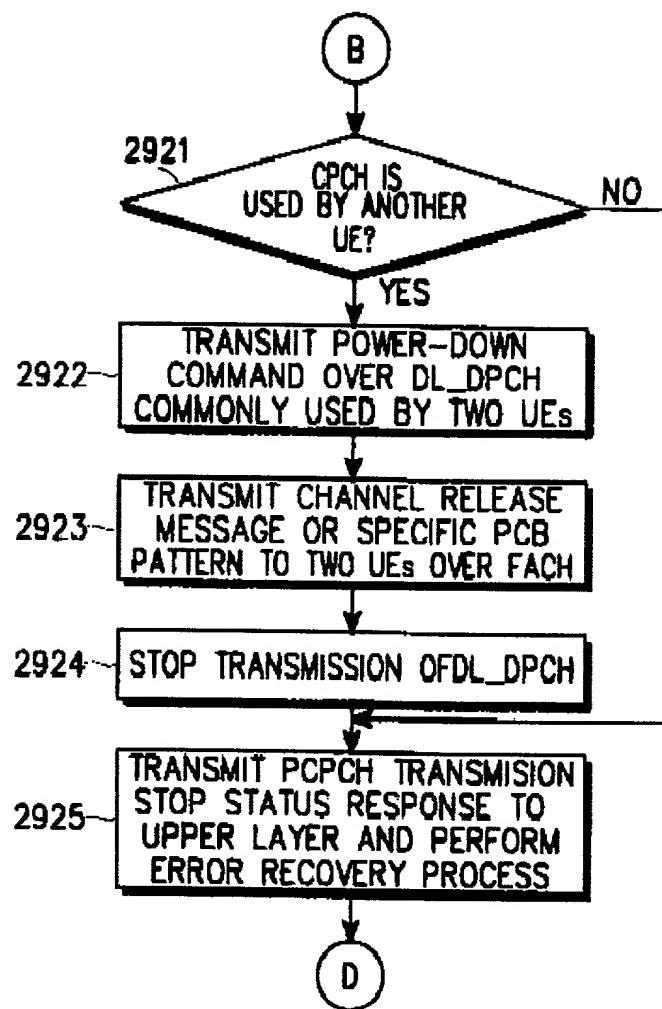


FIG. 29B

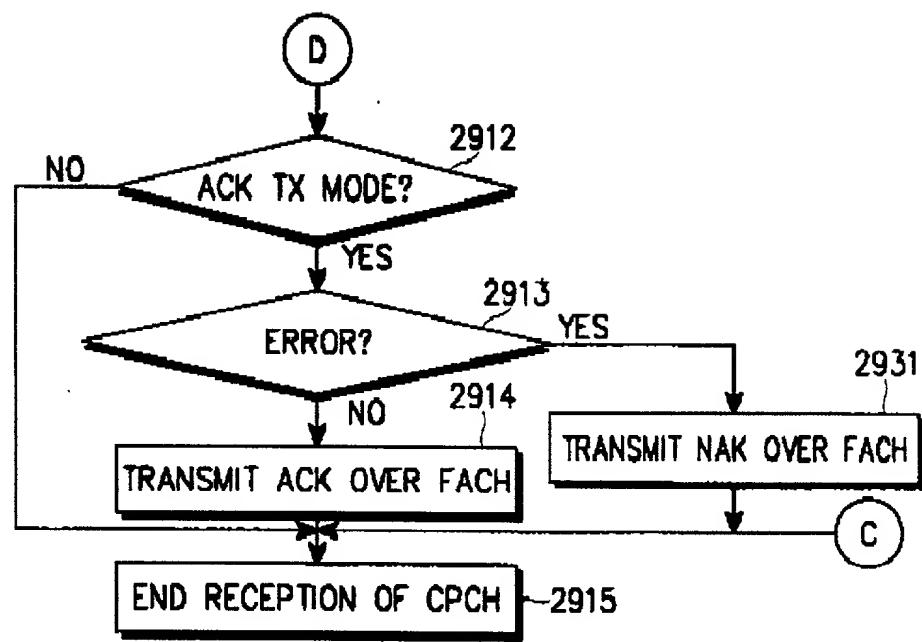


FIG. 29C

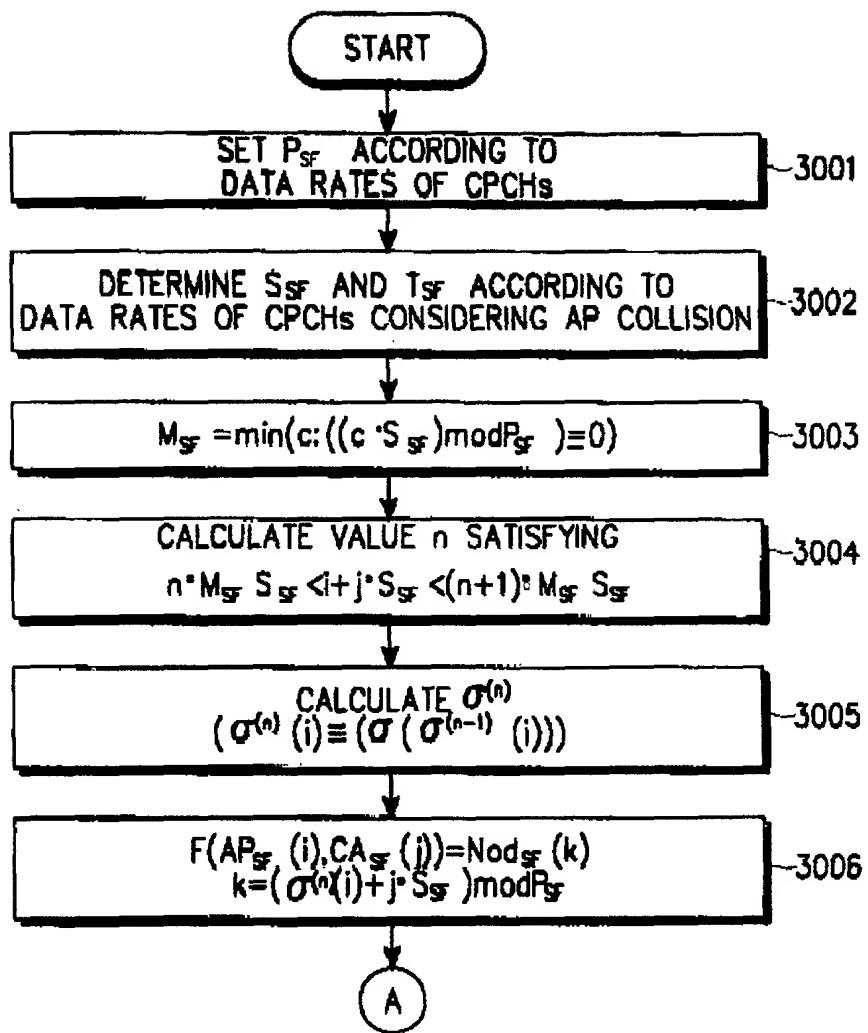


FIG. 30A

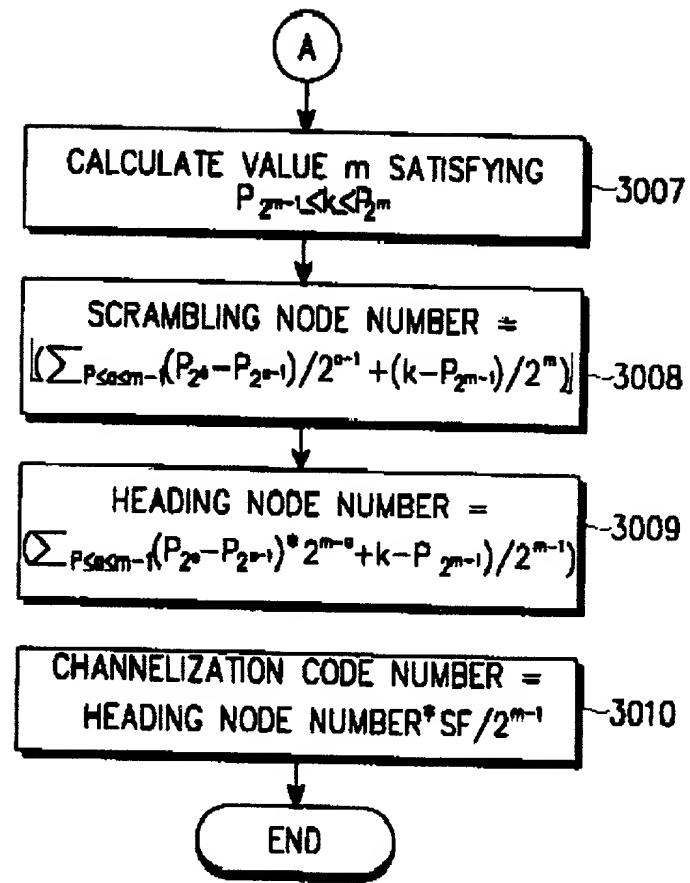


FIG. 30B

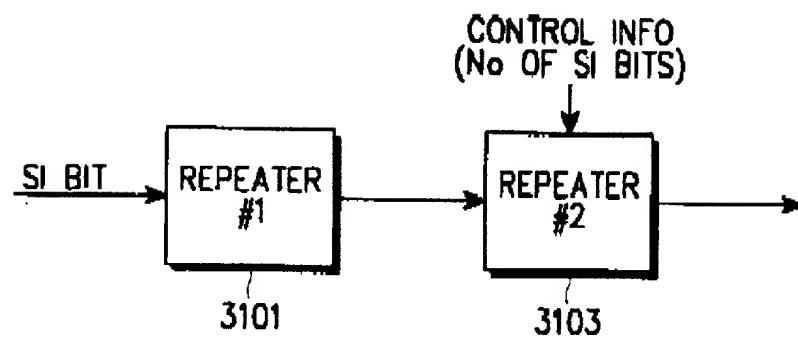


FIG. 31

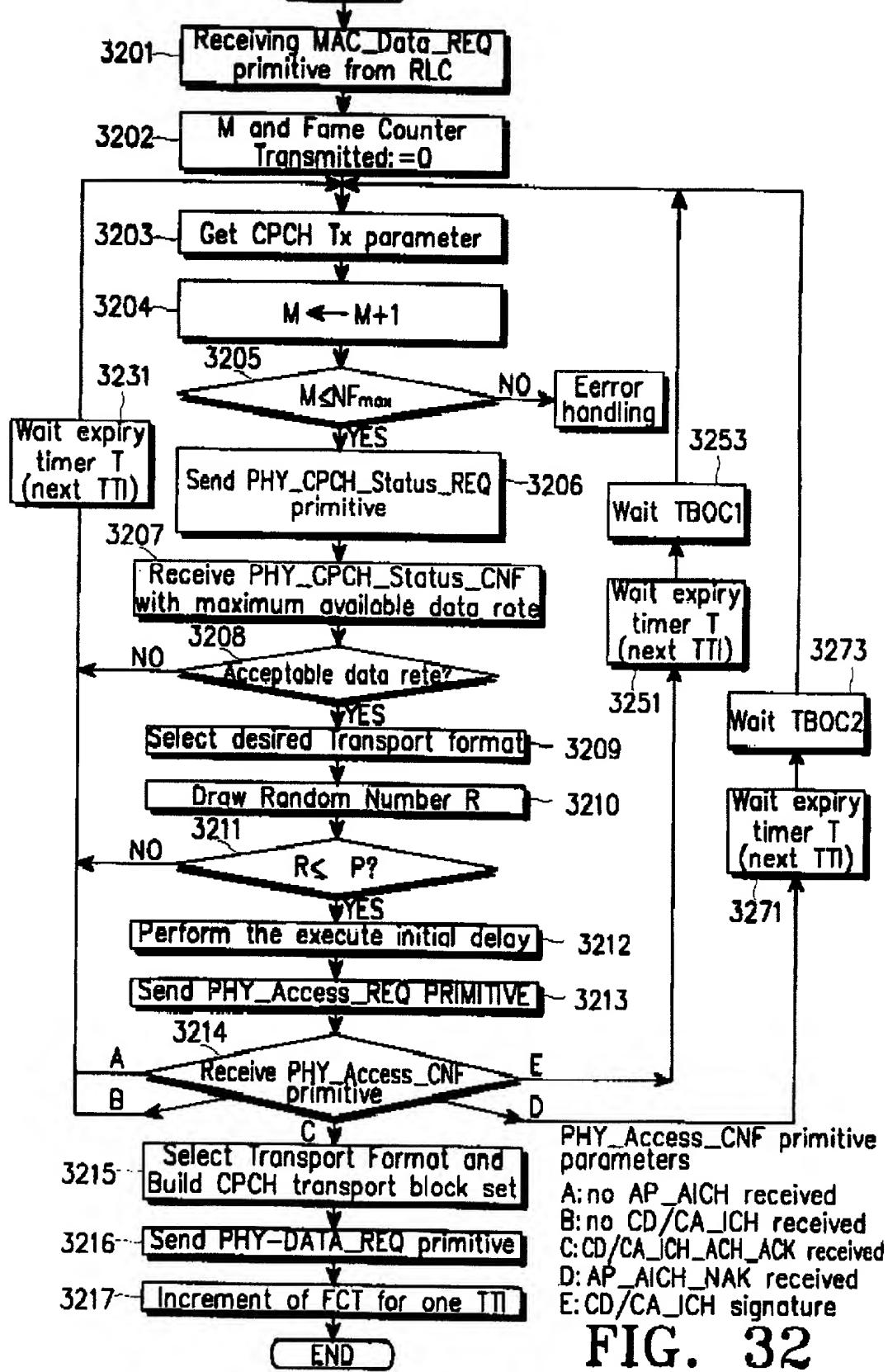


FIG. 32